MITSUBISHI
Q2ASCPU
Q2ASCPU-S1
Q2ASHCPU
Q2ASHCPU-S1

User's Manual

(Hardware)

Thank you for buying the Mitsubishi general-purpose programmable controller MELSEC-QnA Series

Prior to use, please read both this manual and detailed manual thoroughly and familiarize yourself with the product.

MODEL	Q2ASCPU-U(H/W) JE
MODEL CODE	13JT12
IB(NA)-0800140-G(0706) MEE	

QnA MELSEC

REVISIONS

CONTENTS

1. OVERVIEW

This manual explains the performance specifications, names and settings of each part, and the error codes for the Q2ASCPU, Q2ASCPU-S1, Q2ASHCPU and Q2ASHCPU-S1 (hereinafter, Q2ASCPU).

Refer to the Type A1SC24-R2/A1SH/A2SHCPU(S1)/A2ASCPU(S1/S30) User's Manual (Hardware) IB-66468 enclosed with the base unit for details on the Q2ASCPU safety precautions, general specifications, mounting and installation methods, EMC Directives, Low-voltage Directives and the input/output module specifications and connection methods.

When using the Q2ASCPU with the A1S38HB/A1S38HBEU, refer to the Q2AS(H)CPU (S1) User's Manual (Hardware) enclosed with the base unit. (All of the contents of this manual are given in the User's Manual.)

2. PERFORMANCE SPECIFICATION

2.1 Q2ASCPU Module Performance Specification

Performance specification of Q2ASCPU module is as follows:

Item		Model				Remark
		Q2ASCPU	Q2ASCPU-S1	Q2ASHCPU	Q2ASHCPU-S1	
Control method		Repetitive operation of stored program				
I/O control method		Refresh mode				I/O enabled by specifying direct I/O (DX☐, DY☐)
Programming language		Sequence control dedicated language				
		Relay symbol language, logic symbolic language, MELSAP3 (SFC)				
Processing speed (sequence instructions) (us/step)	LD	0.2		0.075		
	MOV	0.6		0.225		
Number of instructions (type)	Sequence instructions	39				
	Basic instructions	230				
	Application instructions	321				
	Special dedicated instructions	171				
Constant scan (ms) (Function that makes scan time constant)		5 to 2000 (configurable in multiple of 5 ms module)				Set parameter values to specify
Memory capacity		Capacity of loading memory cards (2036 kbyte maximum)				
Program capacity	Number of steps (steps)	28 k maximum	60 k maximum	28 k maximum	60 k maximum	
	Number of files (files)	28	60	28	60	
I/O device points (points)		8192 (X/Y0 to 1FFF)				Number of usable points in program

Item	Model				Remark
	Q2ASCPU	Q2ASCPU-S1	Q2ASHCPU	Q2ASHCPU-S1	
I/O points (points)	512 (X/Y0 to 1FF)	1024 (X/Y0 to 3FF)	512 (X/Y0 to 1FF)	1024 (X/Y0 to 3FF)	Number of points accessible to actual I/O modules
Clock function	Year, month, date, hour, minute, second, day of week (auto-detects leap years) Accuracy : -1.7 to +4.9s (TYP. +1.7s) / d at 0 depress centigrade Accuracy : -1.0 to +5.2s (TYP. +2.2s) / d at 25 depress centigrade Accuracy : -7.3 to +2.5s (TYP. -1.9s) / d at 55 depress centigrade				
Allowable momentary power failure period	By power supply module				
5 VDC Internal current consumption (A)	0.3	0.3	0.7	0.7	
Mass (kg)	0.5	0.5	0.5	0.5	
External dimension (mm (inch))	130(H) × 54.5(W) × 110(D) (5.12 × 2.15 × 4.33)				

3. PART NAMES AND SETTINGS

3.1 Part names and Settings
This section describes the name and setting of each part of the module.
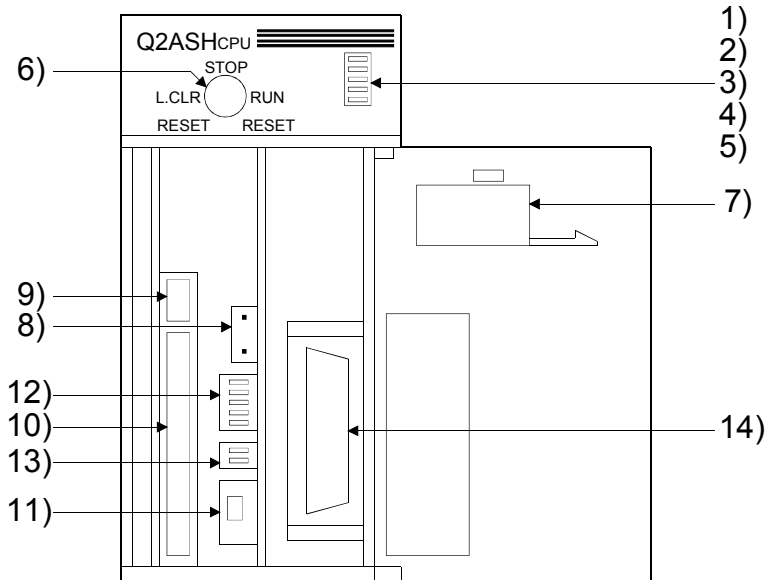
Q2ASCPU, Q2ASCPU-S1, Q2ASHCPU, Q2ASHCPU-S1
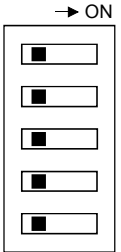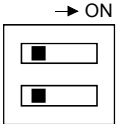
Illustration of the module with the front cover open

No.	Name	Application
1)	RUN LED	This LED indicates the CPU module operating condition. Lit : Operating with the RUN/STOP key switch set to RUN or STEP-RUN. Off : Stopped with the RUN/STOP key switch set to STOP, PAUSE, or STEP-RUN. Or, the CPU module has detected the error that would cause the operation to stop. Flash : The RUN/STOP key switch has been set from STOP to RUN after the program was written in stop mode. The CPU module is not in RUN mode. To engage the CPU module in RUN mode, set the RUN/STOP key switch to RUN, STOP, the RUN. Alternatively, reset the module using the key switch.
2)	ERROR LED	Lit : A self-diagnostic error (other than a battery error) that will not stop operation has been detected. (The parameter has been set to Continue operation at error detection.) Off : Normal Flash : An error that will stop the operation has been detected.
3)	USER LED	Lit : A error has been detected by the CHK instruction, or annunciator F has been turned ON. Off : Normal Flash : The latch clear operation has been executed.
4)	BAT. ALARM LED	Lit : Battery error has occurred due to a drop in the CPU module main unit/memory card battery voltage. Off : Normal
5)	BOOT LED	Lit : The boot operation has been completed. Off : The boot operation has not been executed.

No.	Name	Application
6)	RUN/STOP key switch	RUN/STOP :Executes/stops the operation of the sequence program. L.CLR :Sets the entire data of the latch area specified by the parameter to OFF or 0. Clears the entry of the sampling trace and the status latch. RESET :Executes the hardware reset operation and the reset at an operation error occurrence, and initializes the operation.
7)	Battery (A6BAT)	Backup battery to be used for the internal RAM and the power failure compensation function.
8)	Battery connector pin	Used to connect the battery lead wire. (The lead wire is removed from the connector at shipment in order to prevent battery consumption.)
9)	Memory card EJECT button	Used to eject the memory card from the CPU module.
10)	Memory card loading connector	This connector is used to load the memory card in the CPU module.
11)	Memory card Load/eject switch (LED equipped) ON	This switch setting determines whether or not you can load/eject the memory card during energizing. The factory default setting is OFF. ON :Loading is prohibited. (LED is lit.) OFF :Loading is allowed. (LED is turned off.)

System setting switches 1 (12):

These switches allow you to set the items for the CPU module operation. The factory default setting of all switches is OFF.

SW5 :Boot setting. This switch allows you to select the memory for operation.
ON :Boot operation
OFF :Boot operation is not performed

SW2 to 4 :Parameter area. These switches allow you to select the memory into which to write the parameters.

	Internal RAM	Memory card		SW2 to 4 are valid if SW1 is OFF.
		RAM	ROM	
SW4	OFF	ON	OFF	
SW3	OFF	OFF	ON	
SW2	OFF	OFF	OFF	

SW1 :System protect. Writing to the CPU module and issuing the control instructions are prohibited.
ON :System protect is valid.
OFF :System protect is invalid.

System setting switches 2 (13):

These switches allow you to set the items for CPU module operation. The factory default setting of all switches is OFF.

SW2 :Unused (Fixed to OFF)

SW1 :Peripheral protocol. This switch allows you to select the type of peripheral devices that are connected to the CPU modules peripheral interface.
(Set this switch to ON when you wish to access another stations ACPU from the ACPU peripheral device. The setting becomes effective as soon as you set the switch.)
ON :Peripheral device for the ACPU
OFF :Peripheral device for the Q2ASCPU

| 14) | RS-422 connector | Used to connect a peripheral device. |

3.2 Relationship between switch operation and the LED indication

(1) Writing a program while the CPU module is stopped:

Follow the procedure below to write a program while the CPU module is stopped:

(a) RUN/STOP key switch: STOP
RUN LED: Off ..... CPU module is in STOP mode. → Write a program.

(b) RUN/STOP key switch: RESET
RUN LED: Off ......CPU module is in STOP mode.

(c) RUN/STOP key switch: STOP → RUN
RUN LED: Lit ....CPU module is in RUN mode.

> **POINT**
> * After writing a program (except for online program write), perform reset operation, and then place the CPU module in the RUN status.
> * When remote STOP is switched to RUN, the CPU module is not put in the "PROG CHECK" status but is placed in the RUN status.

(2) Latch clear operation:

Operate the RUN/STOP key switch as follows to execute the latch clear operation:

(a) Turn the RUN/STOP key switch of the CPU module from the "STOP" position to the "L. CLR" position several times (three or four times) to flicker the "USER LED" on the CPU module front.
Normally, the LED flickers when the switch is turned several times (three or four times).
When the "USER LED" flickers, it indicates that latch clear is ready.

(b) After the "USER LED" has flickered, turning the RUN/STOP key switch from the "STOP" position to the "L. CLR" position again executes latch clear and lights up the "USER LED".
If the "USER LED" comes on for two seconds and then goes off, it indicates that latch clear is completed normally.

(c) To cancel latch clear midway, turn the RUN/STOP key switch to the "RUN" position to place the CPU module in the RUN state, or turn it to the "RESET" position to make a reset.

> **POINT**
> * You can make latch clear valid or invalid for each device via the device setting in parameter mode.
> * Instead of using the RUN/STOP key switch, you can also execute the latch clear operation remotely from the peripheral GPP function device.
> (Refer to the Q2AS(H)CPU(S1) User's Manual (Detailed Manual).)

(3) Removing the memory card while the PLC power is on:
   Operate the memory card load/eject switch as described below the memory card while the PLC power is still on:
   (a) Load/eject switch: ON,
      Load/eject switch internal LED:Lit ....Ejecting the memory card is prohibited.
   (b) Load/eject switch: OFF,
      Load/eject switch internal LED:Off ....Ejecting the memory card is allowed.
      ....→ Remove the memory card.

┌───┐
│ POINT   │
├───┤
│ • The load/eject switch internal LED may not be turned off when you are using the memory card for the CPU module system function (such as sampling trace and status latch) or for the program.
│   In this case, quit the corresponding system function or program that is using the memory card. Then, make sure that the load/eject switch internal LED is turned off, and remove the memory card.
│ • Do not turn on the memory card load/eject switch after you have removed the memory card. Otherwise, on error will occur.
│ • When there are parameter-set file registers, local devices or failure history, the memory card cannot be the removed. If the "memory card in/out" switch is turned OFF, the in/out switch built-in LED does not go off. For the file registers, the memory card can be removed when they are set to be unused with the QDRSET(P) instruction.
└───┘

(4) Loading the memory card while the PLC power is on:
   Operate the memory card load/eject switch as described below to load the memory card while the PLC power is still on:
   (a) Load the memory card.
   (b) Load/eject switch: ON,
      Load/eject switch internal LED: Lit ....Ejecting the memory card is prohibited.

┌───┐
│ POINT   │
├───┤
│ • Be sure to turn on the memory card load/eject switch after you have loaded the memory card. Otherwise, you will not be able to use the card.
│ • Since mount processing is performed again after the memory card is inserted, note that the scan time of one scan when mount processing is performed increases by a maximum of 10ms.
└───┘

4. ABOUT FAIL-SAFE CIRCUITS

4.1 Fail-Safe Circuit Concept

When the PLC is powered ON and then OFF, improper outputs may be generated temporarily depending on the delay time and start-up time differences between the PLC power supply and the external power supply for the control target (especially, DC).

For example, if the external power supply for the control target is powered ON and then the PLC is powered ON, the DC output module may generate incorrect outputs temporarily upon the PLC power-ON. Therefore, it is required to build the circuit that energizes the PLC by priority.

The external power failure or PLC failure may lead to the system error.
In order to eliminate the possibility of the system error and ensure fail-safe operation, build the following circuit outside the PLC: emergency circuit, protection circuit and interlock circuit, as they could cause machine damages and accidents due to the abovementioned failures.

An example of system design, which is based on fail-safe concept, is provided on the next page.

$\diamondsuit$ **DANGER**	● Create a safety circuit outside the PLC to ensure the whole system will operate safely even if an external power failure or a PLC failure occurs. Otherwise, incorrect output or malfunction may cause an accident.

● Create a safety circuit outside the PLC to ensure the whole system will operate safely even if an external power failure or a PLC failure occurs.
Otherwise, incorrect output or malfunction may cause an accident.

(1) For an emergency stop circuit, protection circuit and interlock circuit that is designed for incompatible actions such as forward/reverse rotation or for damage prevention such as the upper/lower limit setting in positioning, any of them must be created outside the PLC.

(2) When the PLC detects the following error conditions, it stops the operation and turn off all the outputs.

・ The overcurrent protection device or overvoltage protection device of the power supply module is activated.

・ The PLC CPU detects an error such as a watchdog timer error by the self-diagnostics function.

In the case of an error of a part such as an I/O control part that cannot be detected by the PLC CPU, all the outputs may turn on. In order to make all machines operate safely in such a case, set up a fail-safe circuit or a specific mechanism outside the PLC.

(3) Depending on the failure of the output module's relay or transistor, the output status may remain ON or OFF incorrectly. For output signals that may lead to a serious accident, create an external monitoring circuit.

● Design a circuit so that the external power will be supplied after power-up of the PLC.
Activating the external power supply prior to the PLC may result in an accident due to incorrect output or malfunction.

● If load current more than the rating or overcurrent due to a short circuit in the load has flowed in the output module for a long time, it may cause a fire and smoke. Provide an external safety device such as a fuse.

● For the operation status of each station at a communication error in data link, refer to the respective data link manual. The communication error may result in an accident due to incorrect output or malfunction.

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<td rowspan="1">⬦ DANGER</td>
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● When controlling a running PLC (data modification) by connecting a peripheral device to the CPU module or a PC to a special function module, create an interlock circuit on sequence programs so that the whole system functions safely all the time.

Also, before performing any other controls (e.g. program modification, operating status change (status control)), read the manual carefully and ensure the safety.

In these controls, especially the one from an external device to a PLC in a remote location, some PLC side problem may not be resolved immediately due to failure of data communications.

To prevent this, create an interlock circuit on sequence programs and establish corrective procedures for communication failure between the external device and the PLC CPU.

● When setting up the system, do not allow any empty slot on the base unit.

If any slot is left empty, be sure to use a blank cover (A1SG60) or a dummy module (A1SG62) for it.

When using the extension base unit, A1S52B, A1S55B or A1S58B, attach the included dustproof cover to the module in slot 0.

Otherwise, internal parts of the module may be flied in the short circuit test or when an overcurrent or overvoltage is accidentally applied to external I/O section.

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<td>⚠ CAUTION</td>
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● Do not install the control lines or communication cables together with the main circuit or power lines, or bring them close to each other.

Keep a distance of 100mm (3.9inch) or more between them. Failure to do so may cause malfunctions due to noise.

● When an output module is used to control the lamp load, heater, solenoid valve, etc., a large current (ten times larger than the normal one) may flow at the time that the output status changes from OFF to ON. Take some preventive measures such as replacing the module with the one of a suitable current rating.

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(1) System design circuit example

AC system

Power supply

Transformer

Fuse

CPU module

SM52 —(Ym)
SM403 —(Yn)
XM —(Y1)
—[SM1084]

Program

RA1 —(MC)

Start switch

MC Stop switch

RA2

Input unit
—○ XM

Output unit

Ym —(L)

Yn —(RA1)

Output unit

MC

MC2 —(MC1)

MC1 —(MC2)

Y1 —(RA2)

AC/DC system

Power supply

Transformer

Fuse

DC power supply

CPU module

SM52 —(Ym)
SM403 —(Yn)
XM —(TM)
TM [MC1 N0 M10]

N0 —M10
Program

RA1 —(MC)

Start switch

MC Stop switch

RA2

Input unit
—○ XM

(+)(−)
Fuse

(RA2)

Output unit

Ym —(L)

Yn —(RA1)

Output module

MC MC

MC2 —(MC1)

MC1 —(MC2)

DC power supply established signal input

Start/stop circuit
(Can be started by turning ON of RA1, which is the PLC's RUN output.)

Output for warning (lamp or buzzer)

Turned ON in RUN status by SM403

Switches the power supply to output devices OFF when the system stops:
At emergency stops
At stops on reaching a limit

Interlock circuit
Constructs external interlock circuits for opposing operations such as forward and reverse rotation, and parts that could cause machine damage or accidents.

The setting for TM is the time taken to establish the DC input signal.

Voltage relay recommended

Output for warning (lamp or buzzer)

Turned ON in RUN status by SM403

Switches the power supply to output devices OFF when the system stops:
At emergency stops
At stops on reaching a limit

The procedures used to switch on the power supply are indicated below.

AC system
[1] Switch the power supply ON.
[2] Set the CPU module to RUN.
[3] Switch the start switch ON.
[4] The output devices are driven in accordance with program when the magnetic contactor (MC) turns ON.

AC/DC system
[1] Switch the power supply ON.
[2] Set the CPU module to RUN.
[3] Switch RA2 ON when the DC power supply starts.
[4] Switch the timer (TM) ON when the DC power supply reaches working voltage. (The set value for TM must be the time it takes for 100% establishment of the DC power after RA2 is switched ON. Make this set value 0.5 seconds.)
[5] Switch the start switch ON.
[6] The output devices are driven in accordance with the program when the magnetic contactor (MC) comes ON. (If a voltage relay is used at RA2, no timer (TM) is necessary in the program.)

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(2) Fail-safe measures to cover the possibility of PLC failure

Problems with a CPU module and memory can be detected by the self diagnostics function. However, problems with I/O control area may not be detected by the CPU module.

In such cases, all I/O points turn ON or OFF depending on the problem, and normal operation and safety cannot be maintained.

Though Mitsubishi PLCs are manufactured under strict quality control, they may fail or malfunction due to unspecified reasons. To prevent the whole system failure, machine breakdown, and accidents, build a fail-safe circuit outside the PLC.

Examples of a system and its fail-safe circuitry are described below:

\<System example\>

Output module for fail-safe purpose*1
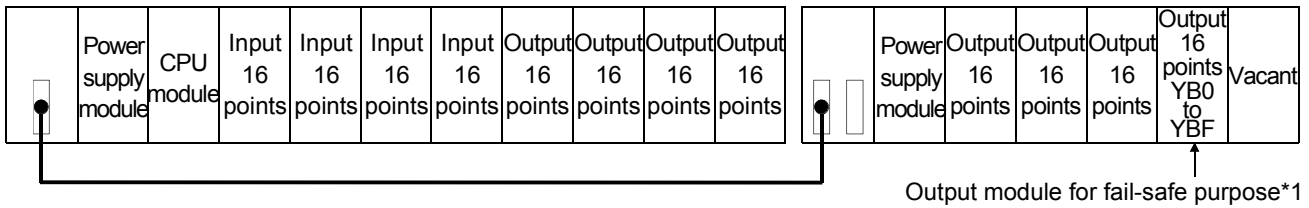
*1: The output module for fail-safe purpose should be mounted on the last slot of the system. (YB0 to YBF in the above system.)
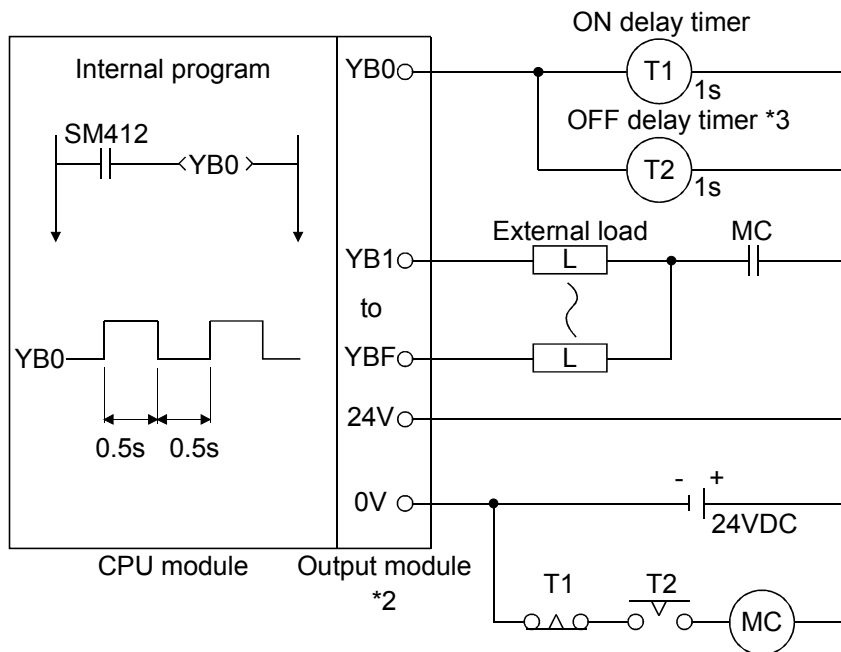
*2: Since YB0 turns ON and OFF alternatively at 0.5 second intervals, use a contactless output module (a transistor is used in the above example).

*3: If an offdelay timer (especially miniature timer) is not available, construct the failsafe circuit using an ondelay timer shown on the next page.

MEMO

5. ERROR CODES

The Q2ASCPU uses the self-diagnostic function to display an error code (LED indicator) and store the error information in the special relay SM and the special register SD, if an error occurs when the power is turned on to the PC or when the PC starts or running.

Also, if an error occurs when a communication request is issued from a peripheral device, special function module or network system, the Q2ASCPU module returns the error code (4000H to 4FFFH) to the request source.

This chapter explains the details of errors that could occur on the Q2ASCPU and how to take a corrective action against them.

REMARK

The error code of the error that occurred when a general data processing request is made from the peripheral device, special function module or network system is not stored into SD0 of the Q2ASCPU.

The error code is returned to the source of the general data processing request.

5.1 Error Code Type

Errors are detected by the self diagnostics function of CPU module or during communication with CPU module.

The following table classifies the errors according to the detection pattern, detection location and error code.

Error detection pattern	Error detection location	Error code	Reference
Detection by the self diagnostics function of CPU module	CPU module	1000 to 10000*1	Section 5.3
Detection at communication with CPU module	CPU module	4000H to 4FFFH	Q2A(S1)/Q3A/Q4ACPU User's Manual
	Serial communication module, etc.	7000H to 7FFFH	Serial Communication User's Manual, etc.
	CC-Link module	B000H to BFFFH	CC-Link System Master/Local Module User's Manual
	Ethernet module	C000H to CFFFH	Ethernet Interface Module User's Manual
	MELSECNET/H network module	F000H to FFFFH	For QnA/Q4AR MELSECNET/10 Network System Reference Manual

*1: CPU module error codes are classified into minor, moderate, major errors as shown below.
- Minor error: Errors that may allow the CPU module to continue the operation, e.g., battery error.
  (Error code: 1300 to 10000)
- Moderate error: Errors that may cause the CPU module to stop the operation, e.g., WDT error.
  (Error code: 1300 to 10000)
- Major error: Errors that may cause the CPU module to stop the operation, e.g., RAM error.
  (Error code: 1000 to 1299)

Determine the error level, i.e. whether the operation can be continued or stopped, by referring to "Operating Statuses of CPU" described in Section 5.3 "Error Code List".

5.2 Reading Error Codes

When an error occurs, the error code or error message, etc., can be read out at GPP function. For details regarding the GPP function operation procedure, refer to the GX Developer OPERATING MANUAL or SW□IVD-GPPQ OPERATING MANUAL.

5.3 Error Code List

The following information deals with error codes and the meanings, causes, and corrective measures of error messages.

<Relevant CPU>

○	: Indicates all the QnACPUs and QCPU.
QCPU	: Indicates all the Q series CPU modules.
Q00J/Q00/Q01	: Indicates the Basic model QCPU.
Qn(H)	: Indicates the High Performance model QCPU.
QnPH	: Indicates the Process CPU.
QnPRH	: Indicates the Redundant CPU.
QnA	: Indicates the QnA series and Q2ASCPU series.
Rem	: Indicates the MELSECNET/H remote I/O modules.

Each CPU module model name: Indicates the relevant specific CPU module.
(Example: Q4AR, Q2AS)

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status RUN	LED Status ERROR	CPU Operation Status	Diagnostic Timing	
1000								
1001								
1002								
1003								
1004								
1005								
1006	MAIN CPU DOWN	–	–	Off	Flicker	Stop	Always	
1007								
1008								
1009								

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3 BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1000			○
	1001			Q00J/Q00/Q01 Qn(H) QnPH QnPRH QnU
	1002			
	1003			
	1004	Runaway or failure of CPU module or failure of main CPU • Malfunctioning due to noise or other reason • Hardware fault	• Take noise reduction measures. • Reset the CPU module and RUN it again.If the same error is displayed again, this suggests a CPU module hardware fault.(Contact your local Mitsubishi representative.)	
	1005			Q00J/Q00/Q01 Qn(H) QnPH QnPRH QnU Rem
	1006			Q00J/Q00/Q01 Qn(H) QnPH QnPRH QnU
	1007			Qn(H) QnPH QnPRH
	1008			
	1009	• A failure is detected on the power supply module, CPU module, main base unit, extension base unit or extension cable. • When using the redundant base unit, the redundant power supply module failure in both systems and/or the redundant base unit failure are detected.	Reset the CPU module and RUN it again.If the same error is detected again, it is considered that the power supply module, CPU module, main base unit, extension base unit or extension cable is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/ Q01*4 Qn(H)*6 QnPH QnPRH QnU

*4 Function version is B or later.
*5 Function version is A.
*6 The module whose first 5 digits of serial No. is 04101 or later.
*7 The module whose first 5 digits of serial No. is 07032 or later.
*8 The module whose first 5 digits of serial No. is 08032 or later.
*9 The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1010	END NOT EXECUTE	–	–	Off	Flicker	Stop	When an END instruction executed	
1020	SFCP. END ERROR	–	–	Off	Flicker	Stop	When SFC program is executed	
1035	MAIN CPU DOWN	–	–	Off	Flicker	Stop	Always	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3 BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1010	Entire program was executed without the execution of an END instruction. • When the END instruction is executed it is read as another instruction code, e.g. due to noise. • The END instruction has been changed to another instruction code somehow.	• Take noise reduction measures. • Reset the CPU module and RUN it again. If the same error is displayed again, this suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	○
	1020	The SFC program cannot be normally terminated due to noise or other reason. • The SFC program cannot be normally terminated due to noise or any similar cause. • The SFC program cannot be normally terminated for any other reason.		Q00J/Q00/ Q01[*4] QnPH QnU
	1035	Runaway or error of the CPU module was detected. • Malfunction due to noise etc. • Hardware failure	• Take measures against noise. • Reset the CPU module and run it again. If the same error is displayed again, the CPU module has hardware failure.(Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnU

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1101	RAM ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset/ When an END instruction executed	
1102							At power ON/ At reset/ When an END instruction executed	
1103							At power ON/ At reset	
							At power ON/ At reset/ When an END instruction executed	
1104	RAM ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	
1105								

*1　CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2　In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3　BAT.ALM LED is displayed at BATTERY ERROR.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1101	The sequence program storing built-in RAM/program memory in the CPU module is faulty.	• Take noise reduction measures. • Reset the CPU module and RUN it again. If the same error is displayed again,this suggests a CPU module hardware fault.(Contact your local Mitsubishi representative.)	○
	1102	The work area RAM in the CPU module is faulty.	• Take noise reduction measures. • Reset the CPU module and RUN it again. If the same error is displayed again,this suggests a CPU module hardware fault.(Contact your local Mitsubishi representative.)	○
		The device memory in the CPU module is faulty.	• Take noise reduction measures. • When indexing is performed, check the value of index register to see if it is within the device range. • Reset the CPU module and RUN it again. If the same error is displayed again,this suggests a CPU module hardware fault.(Contact your local Mitsubishi representative.)	○
	1103	• The device memory in the CPU module is faulty. • The device out of range is accessed due to indexing, and the device for system is overwritten.		Qn(H)^{*8} QnPH^{*8} QnPRH^{*9}
	1104	The address RAM in the CPU module is faulty.		○
	1105	The system RAM in the CPU module is faulty.	• Take noise reduction measures. • Reset the CPU module and RUN it again.If the same error is displayed again, this suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	Q4AR
		The CPU memory in the CPU module is faulty.		Q00J/Q00/Q01 QnU
		The CPU shared memory in the CPU module is faulty.		Qn(H)^{*4} QnPH QnPRH QnU

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1106	RAM ERROR	–	–	Off	Flicker	Stop	STOP→RUN/ When an END instruction executed	
1107	RAM ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	
1108								
1109							Always	
1110	TRK. CIR. ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	
1111		–	–	Off	Flicker	Stop		

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3 BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1106	The battery is dead. The program memory in the CPU module is faulty.	• Check the battery to see if it is dead or not. If dead, replace the battery. • Take noise reduction measures. • Format the program memory, write all files to the PLC, then reset the CPU module, and RUN it again. If the same error is displayed again, the possible cause is a CPU module hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Qn(H) QnPH[*7] QnPRH
	1107	The work area RAM in the CPU module is faulty.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	QnPRH
	1108			
	1109			Qn(H)[*8] QnPH[*8] QnPRH[*9]
	1110	A fault was detected by the initial check of the tracking hardware.		QnPRH
	1111	A tracking hardware fault was detected.		

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1112 1113	TRK. CIR. ERROR	–	–	Off	Flicker	Stop	During running	
1115	TRK. CIR. ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	
1116	TRK. CIR. ERROR	–	–	Off	Flicker	Stop	During running	
1150	RAM ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1112	A tracking hardware fault was detected during running. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws. • The error occurred at a startup since the redundant system startup procedure was not followed.	• Start after checking that the tracking cable is connected. If the same error is displayed again, the cause is the hardware fault of the tracking cable or CPU module. (Please contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again. For details, refer to the QnPRHCPU User's Manual (Redundant System).	QnPRH
	1113			
	1115	A fault was detected by the initial check of the tracking hardware.	This suggests a CPU module hardware fault. (Contact your nearest Mitsubishi representative.)	
	1116	A tracking hardware fault was detected during running. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws. • The error occurred at a startup since the redundant system startup procedure was not followed.	• Start after checking that the tracking cable is connected. If the same error is displayed again, the cause is the hardware fault of the tracking cable or CPU module. (Please contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again. For details, refer to the QnPRHCPU User's Manual (Redundant System).	
	1150	The memory of the CPU module in the Multiple CPU high speed transmission area is faulty.	• Take noise reduction measures. • Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	QnU[*10]

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
1160							At program execution	
1161	RAM ERROR	–	–	Off	Flicker	Stop	At program execution	
1162							At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1160	The program memory in the CPU module is overwritten.	• Take noise reduction measures. • Format the program memory, write all files to the PLC, then reset the CPU module, and RUN it again. If the same error is displayed again, the CPU module has hardware failure. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	
	1161	The data of the device memory built in the CPU module is overwritten.	• Take noise reduction measures. If the same error is displayed again, the CPU module has hardware failure. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	QnU
	1162	The error of the data held by the battery in the CPU module is detected. (It occurs when the automatic format is not set.)	• Take noise reduction measures. • Change the CPU main body or SRAM card battery. If the same error is displayed again, the CPU module has hardware failure. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
1200	OPE. CIRCUIT ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset	
1201								
1202								
1203	OPE. CIRCUIT ERR.	–	–	Off	Flicker	Stop	When an END instruction executed	
1204								
1205								
1206	OPE. CIRCUIT ERR.	–	–	Off	Flicker	Stop	When instruction executed	
1300	FUSE BREAK OFF	Module No. (Slot No.) [For Remote I/ O network] Network No./ Station No.	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	Always	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)

*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.

*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1200	The operation circuit for index modification in the CPU module does not operate normally.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	○
	1201	The hardware (logic) in the CPU module does not operate normally.		
	1202	The operation circuit for sequence processing in the CPU module does not operate normally.		
	1203	The operation circuit for index modification in the CPU module does not operate normally.		Q4AR QnPRH
	1204	The hardware (logic) in the CPU module does not operate normally.		
	1205	The operation circuit for sequence processing in the CPU module does not operate normally.		
	1206	The DSP operation circuit in the CPU module does not operate normally.		Q4AR
	1300	There is an output module with a blown fuse.	• Check FUSE. LED of the output modules and replace the module whose LED is lit. (The module with a blown fuse can also be identified using GX Developer. Check the special registers SD1300 to SD1331 to see if the bit corresponding to the module is "1".) • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	Qn(H) QnPH QnPRH QnU Rem
		There is an output module with a blown fuse.	Check ERR. LED of the output modules and replace the module whose LED is lit. (The module with a blown fuse can also be identified using GX Developer. Check the special registers SD130 to SD137 to see if the bit corresponding to the module is "1".)	Q00J/Q00/Q01

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
1300	FUSE BREAK OFF	Module No. (Slot No.) [For Remote I/ O network] Network No./ Station No.	–	Off/ On	Flicker/ On	Stop/ Continue[1]	Always

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
1300	There is an output module with a blown fuse.	• Check ERR. LED of the output modules and replace the fuse of the module whose LED is lit. • Read the common information of the error using the peripheral device and replace the fuse at the output module corresponding to the numerical value (module No.) reading. Alternatively, monitor special registers SD1300 to SD1331 with the peripheral device and change the fuse of the output module whose bit has a value of "1". • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the grounding status of the GOT.	QnA Q4AR
	• There is an output module with a blown fuse. • External power supply for output load is turned off or disconnected.	• Check ERR. LED of the output modules and replace the module whose LED is lit. • Read the common information of the error using the peripheral device and replace the fuse at the output module corresponding to the numerical value (module No.) reading. Alternatively, monitor special registers SD1300 to SD1331 with the peripheral device and change the fuse of the output module whose bit has a value of "1". • Check whether the external power supply for output load is ON or OFF. • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	Q2AS

*4　Function version is B or later.
*5　Function version is A.
*6　The module whose first 5 digits of serial No. is 04101 or later.
*7　The module whose first 5 digits of serial No. is 07032 or later.
*8　The module whose first 5 digits of serial No. is 08032 or later.
*9　The module whose first 5 digits of serial No. is 09012 or later.
*10　The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
1310	I/O INT. ERROR	–	–	Off	Flicker	Stop	During interrupt	
1311								
1401	SP. UNIT DOWN	Module No. (Slot No.)	–	Off/ On	Flicker/ On	Stop/ Continue[*2]	At power ON/ At reset/When intelligent function module is accessed	
				Off	Flicker	Stop[*2]	At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1310	An interruption has occurred although there is no interrupt module.	Any of the mounted modules is experiencing a hardware fault. Therefore, check the mounted modules and change the faulty module. (Contact your local Mitsubishi representative.)	○
	1311	An interrupt request from other than the interrupt module was detected.	Take action so that an interrupt will not be issued from other than the interrupt module.	Q00J/Q00/ Q01[*4] QnU
		An interrupt request from the module where interrupt pointer setting has not been made in the PLC parameter dialog box was detected.	• Correct the interrupt pointer setting in the PLC system setting of the PLC parameter dialog box. • Take measures so that an interrupt is not issued from the module where the interrupt pointer setting in the PLC system setting of the PLC parameter dialog box has not been made. Correct the interrupt setting of the network parameter. Correct the interrupt setting of the intelligent function module buffer memory. Correct the basic program of the QD51.	Q00J/Q00/ Q01 [*5] QnPRH QnU
	1401	• There was no response from the intelligent function module/special function module in the initial processing. • The size of the buffer memory of the intelligent function module/special function module is invalid. • The unsupported module is mounted.	When the unsupported module is mounted, remove it. When the corresponding module is supported, this suggests the intelligent function module/special function module, CPU module and/or base unit is expecting a hardware fault (Contact your local Mitsubishi representative.)	QCPU Rem
		When PLC parameter I/O allocation was being made, there was no return signal from the special function module during initial processing stage.(When error is generated, the head I/O number of the special function module that corresponds to the common information is stored.)	The CPU module, base unit and/or the special function module that was accessed is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	QnA

*4 Function version is B or later.
*5 Function version is A.
*6 The module whose first 5 digits of serial No. is 04101 or later.
*7 The module whose first 5 digits of serial No. is 07032 or later.
*8 The module whose first 5 digits of serial No. is 08032 or later.
*9 The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1402	SP. UNIT DOWN	Module No. (Slot No.)	Program error location	Off/ On	Flicker/ On	Stop/ Continue[*2]	When an intelligent function module access instruction is executed	
				Off	Flicker	Stop	During execution of FROM/TO instruction set	
1403	SP. UNIT DOWN	Module No. (Slot No.)	–	Off/ On	Flicker/ On	Stop/ Continue[*2]	When an END instruction executed	
				Off/ On	Flicker/ On	Stop/ Continue[*2]	Always	

*1　CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2　In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3　BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1402	The intelligent function module/special function module was accessed in the program, but there was no response.	This suggests the intelligient function module/special function module , CPU module and/or base unit is expecting a hardware fault (Contact your local Mitsubishi representative.)	QCPU Rem
		The special function module was accessed during the execution of a FROM/TO instruction set, but there was no response. (When an error is generated, the program error location corresponding to the individual information is stored.)	The CPU module, base unit and/or the special function module that was accessed is experiencing a hardware fault.(Contact your local Mitsubishi representative.)	QnA
	1403	• The unsupported module is mounted.	When the unsupported module is mounted, remove it. When the corresponding module is supported, this suggests the intelligent function module/special function module , CPU module and/or base unit is expecting a hardware fault (Contact your local Mitsubishi representative.)	QCPU
		• There was no response from the intelligent function module/special function module when the END instruction is executed. • An error is detected at the intelligent function module/special function module. • The I/O module (intelligent function module/special function module) is nearly removed, completely removed, or mounted during running.	The CPU module, base module and/or the intelligent function module/special function module that was accessed is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	QCPU Rem

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
1411	CONTROL-BUS. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	
1412	CONTROL-BUS. ERR.	Module No. (Slot No.)	Program error location	Off	Flicker	Stop	During execution of FROM/TO instruction set	
1413	CONTROL-BUS. ERR.	–	–	Off	Flicker	Stop	Always	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1411	When performing a parameter I/O allocation the intelligent function module/special function module could not be accessed during initial communications. (On error occurring, the head I/O number of the corresponding intelligent function module/special function module is stored in the common information.)	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module/ special function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	○ Rem
	1412	The FROM/TO instruction is not executable, due to a control bus error with the intelligent function module/ special function module. (On error occurring, the program error location is stored in the individual information.)		○
	1413	In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	• Remove the CPU module incompatible with the multiple CPU system from the main base unit, or replace the CPU module incompatible with the multiple CPU system with a CPU module compatible with the multiple CPU system. • The intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/ Q01[*4] Qn(H)[*4] QnPH
		An error is detected on the system bus. • Self-diagnosis error of the system bus. • Self-diagnosis error of the CPU module	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	QCPU Rem

*4 Function version is B or later.
*5 Function version is A.
*6 The module whose first 5 digits of serial No. is 04101 or later.
*7 The module whose first 5 digits of serial No. is 07032 or later.
*8 The module whose first 5 digits of serial No. is 08032 or later.
*9 The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
1414	CONTROL-BUS. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	Always
1415	CONTROL-BUS. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	When an END instruction executed / At power-ON/ At reset/ When an END instruction executed
1416	CONTROL-BUS. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset
1417	CONTROL-BUS. ERR.	–	–	Off	Flicker	Stop	Always

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3 BAT.ALM LED is displayed at BATTERY ERROR.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1414	• Fault of a loaded module was detected. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	• Remove the CPU module incompatible with the multiple CPU system from the main base unit, or replace the CPU module with a CPU module compatible with the multiple CPU system. • Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01[*4] Qn(H)[*4] QnPH QnU
		An error is detected on the system bus.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01[*4] Qn(H) QnPH QnPRH QnU Rem
	1415	Fault of the main or extension base unit was detected.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01 Qn(H)[*4] QnPH QnPRH QnU Rem
				Qn(H)[*8] QnPH[*8]
	1416	System bus fault was detected at power-on or reset.		Qn(H)[*4] QnPH QnU
		In a multiple CPU system, a bus fault was detected at power-on or reset.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00/Q01[*4] QnU
	1417	A reset signal error was detected on the system bus.		QnPRH

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1418	CONTROL-BUS.ERR.	–	–	Off	Flicker	Stop	At power-ON/ At reset/ At Switching execution	
1421	SYS. UNIT DOWN	–	–	Off	Flicker	Stop	Always	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
1418	In the redundant system, at power-on/reset or switching system, the control system cannot access the extension base unit since it failed to acquire the access right.	Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module, the Q6□WRB, or hardware of extension cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH[*9]
1421	Hardware fault at the system management module AS92R.	This suggests a system management module AS92R hardware fault. (Contact your local Mitsubishi representative.)	Q4AR

*4 Function version is B or later.
*5 Function version is A.
*6 The module whose first 5 digits of serial No. is 04101 or later.
*7 The module whose first 5 digits of serial No. is 07032 or later.
*8 The module whose first 5 digits of serial No. is 08032 or later.
*9 The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1430		–						
1431	MULTI-C.BUS ERR.	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	
1432								
1433	MULTI-C.BUS ERR.	Module No. (CPU No.)	–	Off	Flicker	Stop	Always	
1434								
1435								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1430	The error of host CPU is detected in the Multiple CPU high speed bus.	Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnU[*10]
	1431	The communication error with other CPU is detected in the Multiple CPU high speed bus.	• Take noise reduction measures. • Check the main base unit mounting status of the CPU module. • Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	1432	The communication time out with other CPU is detected in the Multiple CPU high speed bus.	Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	1433		• Take noise reduction measures. • Check the main base unit mounting status of the CPU module. • Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	1434			
	1435	The communication error with other CPU is detected in the Multiple CPU high speed bus.		

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1436	MULTI-C.BUS ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset	
1437								
1439	MULTI-C.BUS ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset	
1500	AC/DC DOWN	–	–	On	Off	Continue	Always	
1510	DUAL DC DOWN 5V	–	–	On	On	Continue	Always	
	SINGLE PS. DOWN	Base No./ Power supply No.	–	On	On	Continue	Always	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)

*2   In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.

*3   BAT.ALM LED is displayed at BATTERY ERROR.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1436	The error of the Multiple CPU high speed main base unit is detected. (The error of the Multiple CPU high speed bus is detected.)	Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnU[*10]
	1437		• Take noise reduction measures. • Check the main base unit mounting status of the CPU module. • Reset the CPU module and RUN it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	1439	An error of the multiple CPU high speed main base unit was detected. (An error of the multiple CPU high speed bus was detected.)	Reset the CPU module and run it again. If the same error is displayed again, the CPU module has hardware failure. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	1500	• A momentary power supply interruption has occurred. • The power supply went off.	Check the power supply.	○ Rem
	1510	The power supply voltage (100 to 240VAC) of either of the two power supply modules on the power supply duplexing extension base unit dropped to or below 85% of the rated voltage. (This can be detected from the control system of the redundant system.)	Check the supply voltage of the power supply module. If the voltage is abnormal then replace the power supply module.	Q4AR
		The voltage of one power supply module dropped on the redundant base unit.	Check the power supplied to the redundant power supply modules mounted on the redundant base unit.	Qn(H)[*6] QnPH[*6] QnPRH Rem

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
1520	DC DOWN 5V	–	–	Off	Flicker	Stop	Always	
	SINGLE PS. ERROR	Base No./ Power supply No.	–	On	On	Continue	Always	
1530	DC DOWN 24V	–	–	On	On	Continue	Always	
1600	BATTERY ERROR*3	Drive Name	–	On	Off	Continue	Always	
1601								
1602					On			
1610	FLASH ROM ERROR	–	–	On	On	Continue	When writing to ROM	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 In the QCPU except for remote I/ O module, either error stop or continue can be selected for each intelligent function module by the parameters.
*3 BAT.ALM LED is displayed at BATTERY ERROR.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	1520	The voltage(100 to 240VAC) of the power supply module on the extension base unit dropped to or below 85% of the rated voltage. (This can be detected from the control system of the stand-alone system or redundant system.)	Check the supply voltage of the power supply module. If the voltage is abnormal then replace the power supply module.	Q4AR
		A fault of one redundant power supply module was detected in the redundant power supply system.	Hardware fault of the redundant power supply module. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Qn(H)*6 QnPH*6 QnPRH Rem
	1530	The 24 VDC power supplied to the system management module AS92R has dropped below 90% of the rated voltage. (This can be detected from the control system or standby system of the redundant system.)	Check the 24VDC power supplied to the system management module AS92R.	Q4AR
	1600	• The battery voltage in the CPU module has dropped below stipulated level. • The lead connector of the CPU module battery is not connected.	• Change the battery. • If the battery is for program memory, standard RAM or for the back-up power function, install a lead connector.	○
	1601	Voltage of the battery on memory card 1 has dropped below stipulated level.	Change the battery.	QnA Qn(H) QnPH QnPRH QnU
	1602	Voltage of the battery on memory card 2 has dropped below stipulated level.	Change the battery.	QnA
	1610	The number of writing to flash ROM (standard ROM and system securement area) exceeds 100,000 times. (Number of writings >100,000 times)	Change the CPU module.	QnU

*4   Function version is B or later.
*5   Function version is A.
*6   The module whose first 5 digits of serial No. is 04101 or later.
*7   The module whose first 5 digits of serial No. is 07032 or later.
*8   The module whose first 5 digits of serial No. is 08032 or later.
*9   The module whose first 5 digits of serial No. is 09012 or later.
*10 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2000	UNIT VERIFY ERR.	Module No. (Slot No.) [For Remote I/O network] Network No./ Station No.	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When an END instruction executed	
2001	UNIT VERIFY ERR.	Module No. (CPU No.)	–	Off/ On	Flicker/ On	Stop/ Continue[*2]	When an END instruction executed	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2000	In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	Replace the CPU module incompatible with the multiple CPU system with a CPU module compatible with the multiple CPU system.	Qn(H)[*3] QnPH
		The I/O module status is different from the I/O module information at power ON. • I/O module (or intelligent function module) is not installed properly or installed on the base unit.	Read the error common information at the GX Developer, and check and/or change the module that corresponds to the numerical value (module number) there. Alternatively, monitor special registers SD150 to SD157 using GX Developer, and check and replace the module where the bit of its data is "1".	Q00J/Q00/Q01
		I/O module information power ON is changed. • I/O module (or intelligent function module/special function module) not installed properly or installed on the base unit.	• Read the common information of the error using the peripheral device, and check and/or change the module that corresponds to the numerical value (module number) there. • Alternatively, monitor the special registers SD1400 to SD1431 at a peripheral device, and change the fuse at the output module whose bit has a value of "1". • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the grounding status of the GOT.	QnA Qn(H) QnPH QnPRH QnU Rem
	2001	During operation, a module was mounted on the slot where the empty setting of the CPU module was made.	During operation, do not mount a module on the slot where the empty setting of the CPU module was made.	Q00J/Q00/ Q01[*3] QnU

*3 The function version is B or later.
*4 The function version is A.
*5 The module whose first 5 digits of serial No. is 04101 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2010 2011	BASE LAY ERROR	Base No.	–	Off	Flicker	Stop	At power ON/ At reset	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2010	• More than applicable number of extension base units have been used. • When a GOT was bus-connected, the CPU module was reset while the power of the GOT was OFF.	• Use the allowable number of extension base units or less. • Power on the PLC and GOT again.	Q00J/Q00/ Q01[*3] QnPRH Q02U
	2011	The QA1S6□B or QA6□B was used as the base unit.	Do not use the QA1S6□B or QA6□B as the base unit.	Q00J/Q00/ Q01[*3] QnPRH QnU

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2012	BASE LAY ERROR	Base No.	–	Off	Flicker	Stop	At power ON/ At reset	
2013								
2020	EXT.CABLE ERR.	–	–	Off	Flicker	Stop	At power-ON/ At reset/ When an END instruction executed	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2012	The GOT is bus-connected to the main base unit of the redundant system. The following errors are detected in the CPU redundant system compatible with the extension base unit. • The base unit other than the Q6□WRB is connected to the extension stage No.1. • The base unit is connected to any one of the extension stages No.2 to No.7, although the Q6□WRB does not exist in the extension stage No.1 . • The other system CPU module is incompatible with the extension base unit. • The Q5□B, QA1S6□B, or QA6□B is connected. • The number of slots of the main base unit for both systems is different. Information of the Q6□WRB cannot be read correctly.	• Remove a bus connection cable for GOT connection connected to the main base unit. • Use the Q6□WRB (fixed to the extension stage No.1) • Use the CPU module compatible with the extension base unit for the other system. • Do not use the Q5□B, QA1S6□B, or QA6□B for the base unit. • Use the main base unit which has the same number of slots. • Hardware failure of the Q6□WRB. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH[*6]
	2013	Stage number of the Q6□WRB is recognized as other than extension stage No.1 in the redundant system.	Hardware failure of the Q6□WRB. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	
	2020	The following errors are detected in the redundant system. • At power-on/reset, the standby system has detected the error in the path between the control system and the Q6□WRB. • The standby system has detected the error in the path between the host system CPU and the Q6□WRB at END processing.	Check to see if the extension cable between the main base unit and the Q6□WRB is connected correctly. If not, connect it after turning OFF the main base unit where the extension cable will be connected. If the cable is connected correctly, hardware of the CPU module, Q6□WRB, or extension cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2100	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		The slot to which the QI60 is mounted is set to other than Inteli (intelligent function module) or Interrupt (interrupt module) in the I/O assignment of PLC parameter.	Make setting again to match the PLC parameter I/O assignment with the actual loading status.	Qn(H)[*3] QnPH QnPRH
	2100	• In the I/O assignment setting of PLC parameter, Inteli (intelligent function module) was allocated to an I/O module or vice versa. • In the I/O assignment setting of PLC parameter, a module other than CPU (or nothing) was allocated to the location of a CPU module or vice versa. • In the I/O assignment setting of the PLC parameter, switch setting was made to the module that has no switch setting. • In the I/O assignment setting of the PLC parameter dialog box, the number of points assigned to the intelligent function module is less than the number of points of the mounted module.	• Make the PLC parameter's I/O assignment setting again so it is consistent with the actual status of the intelligent function module and the CPU module. • Delete the switch setting in the I/O assignment setting of the PLC parameter.	Qn(H) QnPH QnPRH QnU Rem
		• In the parameter I/O allocation settings, an Inteli (intelligent function module) was allocated to a location reserved for an I/O module or vice versa. • In the parameter I/O allocation settings, a module other than CPU (or nothing) was allocated to a location reserved for a CPU module or vice versa. • In the I/O assignment setting of the PLC parameter dialog box, the number of points assigned to the intelligent function module is less than the number of points of the mounted module.	Reset the parameter I/O allocation setting to conform to the actual status of the intelligent function module and the CPU module.	Q00J/Q00/Q01

*3 The function version is B or later.
*4 The function version is A.
*5 The module whose first 5 digits of serial No. is 04101 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
2100	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset
2101	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset
2102							

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2100	In PLC parameter I/O allocation settings, a special function module was allocated to a location reserved for an I/O module. Or, the opposite has happened.	Reset the PLC parameter I/O allocation setting to conform with the actual status of the special function modules.	QnA
	2101	13 or more A-series special function modules (except for the A1SI61) that can initiate an interrupt to the CPU module have been installed.	Reduce the A series special function modules (except the A1SI61) that can make an interrupt start to the CPU module to 12 or less.	Qn(H)
		13 or more special function modules (not counting the A(1S)I61) capable of sending an interrupt to the CPU module have been installed.	Keep the number of special function modules that can initiate an interrupt (with the exception of the A(1S)I61 module) to 12 or fewer.	QnA
	2102	Seven or more A1SD51S have been installed.	Keep the number of A1SD51S to six or fewer.	Qn(H)
		Seven or more serial communication modules (excludes A (1S) J71QC24) have been installed.	Keep the number of serial communication modules (excludes A(1S)J71QU24) installed to six or fewer.	QnA

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2103	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	
2104 2105	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2103	• Two or more QI60/A1SI61 modules are mounted in a single CPU system. • Two or more QI60/A1SI61 modules are set to the same control CPU in a multiple CPU system. • Two or more A1SI61 modules are loaded in a multiple CPU system.	• Reduce the number of QI60/A1SI61 modules mounted in the single CPU system to one. • Change the number of QI60/A1SI61 modules set to the same control CPU to only one in the multiple CPU system. • Reduce the number of A1SI61 modules to only one in the multiple CPU system. When using an interrupt module with each QCPU in a multiple CPU system, replace it with the QI60. (Use one A1SI61 module + max. three QI60 modules or only the QI60 modules.)	Qn(H)[*3] QnPH
		Two or more QI60, A1SI61 interrupt modules have been mounted.	Install only 1 QI60, A(1S)I61 module.	Qn(H) QnPRH
		The QI60 is mounted.	Remove the QI60.	Rem
		Two or more A1SI61 interrupt modules have been mounted.	Install only 1 AI61 module.	QnA
		Two or more QI60 modules are mounted.	Reduce the QI61 modules to one.	Q00J/Q00/Q01[*5]
		Two or more QI60 modules where interrupt pointer setting has not been made are mounted.	• Reduce the QI60 modules to one. • Make interrupt pointer setting to the second QI60 module and later.	Q00J/Q00/Q01[*3] QnU
	2104	At the MELSECNET/MINI auto refresh network parameter settings, the module allocation that was set is different from the actual module models at the station numbers in the link system.	Reset the network parameter MELSECNET/MINI auto refresh unit module allocation setting so that it conforms to the station number of the module that is actually linked.	
	2105	There are too many special function modules that can use dedicated instructions allocated (number of modules installed). (The total of the figures indicated below is above 1344.) (AD59 modules installed × 5) (AD57(S1)/AD58 modules installed × 8) (AJ71C24(S3/S6/S8) modules installed × 10) (AJ71UC24 modules installed × 10) (AJ71C21(S1) modules installed × 29) (AJ71PT32-S3/AJ71T32-S3 modules installed × 125) * (AJ71QC24(R2,R4) modules installed × 29) (AJ71ID1(2)-R4 modules installed × 8) +(AD75 modules installed × 12) total > 1344 *: When the expansion mode is used.	Reduce the number of special function modules installed.	QnA

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2106	SP.UNIT LAY ERR.	Module No.	–	Off	Flicker	Stop	At power ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2106	• Three or more MELSECNET/H and MELSECNET/G modules in total are mounted in the entire system. • Three or more Ethernet interface modules are mounted in the entire system.	• Reduce the MELSECNET/H and MELSECNET/G modules up to two or less in the entire system. • Reduce the Ethernet interface modules up to two or less in the entire system.	Q02U
		• Five or more MELSECNET/H and MELSECNET/G modules in total are mounted in the entire system. • Five or more Ethernet interface modules are mounted in the entire system.	• Reduce the MELSECNET/H and MELSECNET/G modules up to four or less in the entire system. • Reduce the Ethernet interface modules up to four or less in the entire system.	QnU[*7]
		• Three or more MELSECNET/G modules are mounted in the entire system. • Five or more MELSECNET/H and MELSECNET/G modules in total are mounted in the entire system.	• Reduce the MELSECNET/G modules up to two or less in the entire system. • Reduce the total number of the MELSECNET/H and MELSECNET/G modules up to four or less in the entire system.	Qn(H)[*6]

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2106	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		• Five or more MELSECNET/H modules have been installed. • Five or more Q series Ethernet interface modules have been installed.	• Reduce the number of MELSECNET/H modules to four or less. • Reduce the number of Q series Ethernet modules to four or less.	Qn(H) QnPH QnPRH Rem
		• Two or more MELSECNET/H modules were installed. • Two or more Q series Ethernet modules were installed. • Three or more Q series CC-Link modules were installed.	• Reduce the MELSECNET/H modules to one or less. • Reduce the Q series Ethernet modules to one or less. • Reduce the Q series CC-Link modules to two or less.	Q00J/Q00/Q01
		• The same network number or same station number is duplicated in the MELSECNET/H network system.	• Check the network number and station number.	QCPU Rem
	2106	• Five or more AJ71QLP21 & AJ71QBR11 modules are installed. • Three or more AJ71AP21/R21 & AJ71AT21B modules are installed. • The total number of installed AJ71QLP21, AJ71QBR11, AJ71AP21/R21, and AJ71AT21B modules exceeds five. • The same network numbers or identical station numbers exist in the MELSECNET/10 network system. • Two or more master or load stations exist simultaneously at the MELSECNET(II) or MELSECNET/B data link system.	• Reduce the AJ71QLP21 and AJ71QBR11 modules to four or less. • Reduce the AJ71AP21/R21 and AJ71AT21B modules to two or less. • Reduce the AJ71QLP21, AJ71QBR11, AJ71AP21/R21 and AJ71AT21B modules to a total of four or less. • Check the network Nos. and station Nos. • Check the station Nos.	QnA

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2107	SP. UNIT LAY ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset	
2108								
2109						Stop/ Continue[*2]		
2110	SP. UNIT ERROR	Module No. (Slot No.)	Program error location	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed	
2111								

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2107	The start X/Y set in the PLC parameter's I/O assignment settings is overlapped with the one for another module.	Make the PLC parameter's I/O assignment setting again so it is consistent with the actual status of the intelligent function module/special function modules.	○ Rem
	2108	• Network module A1SJ71LP21, A1SJ71BR11, A1SJ71AP21, A1SJ71AR21, or A1SJ71AT21B dedicated for the A2USCPU has been installed. • Network module A1SJ71QLP21 or A1SJ71QBR11 dedicated for the Q2AS has been installed.	Replace the network module to MELSECNET/H module.	Qn(H)
		A(1S)J71LP21 or A(1S)J71BR11 for use with the AnUCPU network module has been installed.	Replace the network module to A(1S)J71QLP21 or A(1S)J71QBR11.	QnA
	2109	The control system and standby system module configurations are different when a redundant system is in the backup mode.	Check the module configuration of the standby system.	Q4AR
	2110	• The location designated by the FROM/TO instruction set is not the intelligent function module/special function module. • The module that does not include buffer memory has been specified by the FROM/TO instruction. • The intelligent function module/special function module, Network module being accessed is faulty. • Station not loaded was specified using the instruction whose target was the CPU share memory.	• Read the individual information of the error using the GX Developer, check the FROM/TO instruction that corresponds to that numerical value (program error location), and correct when necessary. • The intelligent function module/special function module that was accessed is experiencing a hardware fault. Therefore, change the faulty module. Alternatively, contact your local Mitsubishi representative.	Q00J/Q00/Q01 Qn(H)[*3] QnPH QnPRH QnU
	2111	• The location designated by a link direct device (J□\□) is not a network module. • The I/O module (intelligent function module/special function module) was nearly removed, completely removed, or mounted during running.		○

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2112	SP. UNIT ERROR	Module No. (Slot No.)	Program error location	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed/ STOP → RUN	
2113	SP. UNIT ERROR	FFFF$_H$ (fixed)	Program error location	Off/ On	Flicker/ On	Stop/ Continue[*2]	When instruction executed/ STOP → RUN	

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2112	• The module other than special function module is specified by the special function module dedicated instruction. Or, it is not the corresponding special function module. • The module model specified by the special function module dedicated instruction and that specified by the parameter I/O assignment is different.	• Read the individual information of the error using a peripheral device, and check the special function module dedicated instruction (network instruction) that corresponds to the value (program error part) to make modification. • Set the module model by PLC parameter I/O assignment according to the special function module dedicated instruction setting. Example) Although AJ71QC24N is used actually, AJ71QC24 is set.	QnA
		• The module other than intelligent function module/special function module is specified by the intelligent function module/special function module dedicated instruction. Or, it is not the corresponding intelligent function module/special function module. • There is no network No. specified by the network dedicated instruction. Or the relay target network does not exit.	Read the individual information of the error using a peripheral device, and check the special function module / special function module dedicated instruction (network instruction) that corresponds to the value (program error part) to make modification.	QCPU Rem
	2113	The module other than network module is specified bythe network dedicated instruction.		QnA Qn(H) QnPH

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2114							When instruction executed/ STOP → RUN	
2115								
2116	SP. UNIT ERROR	Module No. (Slot No.)	Program error location	Off/ On	Flicker/ On	Stop/ Continue		
2117								
2118							When instruction executed	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2114	An instruction, which on execution specifies other stations, has been used for specifying the host CPU. (An instruction that does not allow the host CPU to be specified).	Read the individual information of the error using the GX Developer, check the program corresponding that value (program error location), and make correction.	Q00J/Q00/ Q01[*3] Qn(H)[*3] QnPH QnU
	2115	An instruction, which on execution specifies the host CPU, has been used for specifying other CPUs. (An instruction that does not allow other stations to be specified).		Q00J/Q00/ Q01[*3] Qn(H)[*3] QnPH
	2116	• An instruction that does not allow the .under the control of another CPU to be specified is being used for a similar task. • Instruction was executed for the A or QnA module under control of another CPU.		Q00J/Q00/ Q01[*3] Qn(H)[*3] QnPH QnU
	2117	A CPU module that cannot be specified in the instruction dedicated to the multiple CPU system was specified.		Q00J/Q00/ Q01[*3] Qn(H)[*3] QnPH QnU
	2118	When the online module change setting is set to be "enabled" in the PLC parameter in a multiple CPU system, intelligent function module controlled by other CPU using the FROM instruction/ intelligent function module device (U□\G□) is specified.	• When performing the online module change in a multiple CPU system, correct the program so that access will not be made to the intelligent function module controlled by the other CPU. • When accessing the intelligent function module controlled by the other CPU in a multiple CPU system, set the online module change setting to be "disabled" by parameter.	Qn(H)[*3] QnPH QnU

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2120	SP. UNIT LAY ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset	
2121								
2122								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
2120	The locations of the Q5□B/Q6□B and QA1S6□B/QA6□B are improper.	Check the location of the base unit.	Q00J/Q00/ Q01[*4] Qn(H) QnPH
2121	The CPU module is installed to other than the CPU slot and slots 0 to 2.	Check the loading position of the CPU module and reinstall it at the correct slot.	Qn(H) QnPH Rem
2122	The QA1S6□B/QA6□B are used for the main base unit.	Replace the main base unit with a usable one.	Qn(H) QnPH QnPRH Rem

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
2124	SP. UNIT LAY ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2124	• A module is mounted on the 65th slot or later slot. • A module is mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • A module is mounted on the slot whose number of I/O points exceeds 4096 points. • A module is mounted on the slot whose number of I/O points strides 4096 points.	• Remove the module mounted on the 65th slot or later slot. • Remove the module mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • Remove the module mounted on the slot whose number of I/O points exceeds 4096 points. • Replace the module with the one whose number of occupied points does not exceed 4096 points.	Qn(H) QnPH QnPRH QnU[*7] Rem
		• A module is mounted on the 37th slot or later slot. • A module is mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • A module is mounted on the slot whose number of I/O points exceeds 2048 points. • A module is mounted on the slot whose number of I/O points strides 2048 points.	• Remove the module mounted on the 37th slot or later slot. • Remove the module mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • Remove the module mounted on the slot whose number of I/O points exceeds 2048 points. • Replace the module with the one whose number of occupied points does not exceed 2048 points.	Q02U
		• A module is mounted on the 25th slot or later slot. (The 17th slot or later slot for the Q00J.) • A module is mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • A module is mounted on the slot whose number of I/O points exceeds 1024 points. (256 points for the Q00J.) • A module is mounted on the slot whose number of I/O points strides 1024 points. (256 points for the Q00J.)	• Remove the module mounted on the 25th slot or later slot. (The 17th slot or later slot for the Q00J.) • Remove the module mounted on the slot whose number is greater than the number of slots specified at [Slots] in [Standard setting] of the base setting. • Remove the module mounted on the slot whose number of I/O points exceeds 1024 points. (256 points for the Q00J.) • Replace the module with the one whose number of occupied points does not exceed 1024 points. (256 points for the Q00J.)	Q00J/Q00/Q01
		5 or more extension base units were added. (3 bases for Q00J)	Remove 5 or more extension base units. (3 bases for Q00J)	Q00J/Q00/ Q01[*4]

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
2125	SP. UNIT LAY. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset
2126							
2128	SP.UNIT LAY ERR.	Module No.	–	Off	Flicker	Stop	At power-ON/ At reset
2150	SP. UNIT VER. ERR.	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset/ At PLC writing
2151							

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2125	• A module which the QCPU cannot recognise has been installed. • There was no response form the intelligent function module/special function module.	• Install a usable module. • The intelligent function module/special function module is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	QCPU Rem
	2126	CPU module locations in a multiple CPU system are either of the following. • There are empty slots between the QCPU and QCPU/motion controller. • A module other than the High Performance model QCPU/Process CPU (including the motion controller) is mounted on the left-hand side of the High Performance model QCPU/Process CPU.	• Mount modules on the available slots so that the empty slots will be located on the right-hand side of the CPU module.) • Remove the module mounted on the left-hand side of the High Performance model QCPU/Process CPU, and mount the High Performance model QCPU/Process CPU on the empty slot. Mount the motion CPU on the right-hand side of the High Performance model QCPU/Process CPU.	Qn(H)^{*3} QnPH
	2128	The unusable module is mounted on the extension base unit in the redundant system.	• Remove the unusable module from the extension base unit.	QnPRH^{*6}
	2150	In a multiple CPU system, the control CPU of the intelligent function module incompatible with the multiple CPU system is set to other than CPU No.1.	• Change the intelligent function module for the one compatible with the multiple CPU system (function version B). • Change the setting of the control CPU of the intelligent function module incompatible with the multiple CPU system to CPU No.1.	Q00J/Q00/ Q01 QnPH QuU
	2151	Either of the following modules incompatible with the redundant system has been mounted in a redundant system. • MELSECNET/H • Ethernet	Use either of the following modules compatible with the redundant system. • MELSECNET/H • Ethernet	QnPRH

*3　The function version is B or later.
*4　The function version is A.
*5　The module whose first 5 digits of serial No. is 04101 or later.
*6　The module whose first 5 digits of serial No. is 09012 or later.
*7　The Universal model QCPU except the Q02UCPU.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2200	MISSING PARA.	Drive Name	–	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
2210	BOOT ERROR	Drive name	–	Off	Flicker	Stop	At power ON/ At reset	
2211								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2200	There is no parameter file in the drive specified as valid parameter drive by the DIP switches.	• Check and correct the valid parameter drive settings made by the DIP switches. • Set the parameter file to the drive specified as valid parameter drive by the DIP switches.	Qn(H) QnPH QnPRH
		There is no parameter file at the program memory.	Set the parameter file to the program memory.	Q00J/Q00/Q01
		Parameter file does not exist in all drives where parameters will be valid.	Set a parameter file in a drive to be valid.	QuU
	2210	The contents of the boot file are incorrect.	Check the boot setting.	Q00J/Q00/Q01[3] Qn(H) QnPH QnPRH QnU
		There is no boot file in the drive designated by the parameter enabled drive switch even though the Boot DIP switch is ON.	Check and correct the valid parameter drive settings made by the DIP switches. Set the boot file to the drive specified by the parameter drive DIP switches.	QnA
	2211	File formatting is failed at a boot.	• Reboot. • CPU module hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Qn(H) QnPRH QnU

[3]   The function version is B or later.
[4]   The function version is A.
[5]   The module whose first 5 digits of serial No. is 04101 or later.
[6]   The module whose first 5 digits of serial No. is 09012 or later.
[7]   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
2220	RESTORE ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	
2221								
2300	ICM. OPE. ERROR	Drive name	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When memory card is inserted or removed	
2301								
2302								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2220	• The device information (number of points) backuped by the device data backup function is different from the number of device points of the PLC parameter. After this error occurred, perform restore per power-on/reset until the number of device points is identical to the number of device points in the PLC parameter, or until the backup data is deleted.	• Set the number of device points at the time of backup to the device point setting in [PLC parameter]. Then, turn ON from OFF power supply, or reset the CPU and cancel reset. • Delete the backuped data, and turn ON from OFF power supply, or reset the CPU and cancel reset.	QnU
	2221	• The device information backuped by the device data backup function is incomplete. (Turning power supply OFF or reset is suspected.) Do not return the data when this error occurs. Also, delete the incomplete device information at the time of this error occurrence.	Reset the CPU module and run it again.	QnU
	2300	• A memory card was removed without switching the memory card in/out switch OFF. • The memory card in/out switch is turned ON although a memory card is not actually installed.	• Remove memory card after placing the memory card in/out switch OFF. • Turn on the card insert switch after inserting a memory card.	QnA Qn(H) QnPH QnPRH QnU
	2301	• The memory card has not been formatted. • Memory card format status is incorrect. • The QCPU file does not exist in the Flash card.	• Format memory card. • Reformat memory card. • Write the QCPU file to the Flash card.	
		SRAM card failure is detected. (It occurs when automatic format is not set.)	Format SRAM card after changing battery of SRAM card.	QnU
	2302	A memory card that cannot be used with the CPU module has been installed.	• Format memory card. • Reformat memory card. • Check memory card.	QnA Qn(H) QnPH QnPRH QnU

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2400	FILE SET ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ At PLC writing	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2400	Automatic write to standard ROM was performed on the CPU module that is incompatible with automatic write to standard ROM. (Memory card where automatic write to standard ROM was selected in the boot file was fitted and the parameter enable drive was set to the memory card.)	• Execute automatic write to standard ROM on the CPU module which is compatible with automatic write to standard ROM. • Using GX Developer, perform write of parameters and programs to standard ROM. • Change the memory card for the one where automatic write to standard ROM has not been set, and perform boot operation from the memory card.	Qn(H)*3 QnPH QnPRH
		The file designated at the PLC file settings in the parameters cannot be found.	• Read the individual information of the error using peripheral device, check to be sure that the parameter drive name and file name correspond to the numerical values there (parameter number), and correct. • Create a file created using parameters, and load it to the CPU module.	○
		The Ethernet parameter that was added for QnACPU, with the function version "B," has been set to QnACPU without the function version "B."	Change to QnACPU with the function version "B". Delete the Ethernet parameter.	QnA

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2401	FILE SET ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ At PLC writing	
2402								

*1　CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2　Either error stop or continue can be selected for each module by the parameters.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2401	Program memory capacity was exceeded by performing boot operation or automatic write to standard ROM.	• Check and correct the parameters (boot setting). • Delete unnecessary files in the program memory. • Choose "Clear program memory" for boot in the parameter so that boot is started after the program memory is cleared.	Qn(H)[*3] QnPH QnPRH
		Program memory capacity was exceeded by performing boot operation.		QnU
		The file specified by parameters cannot be made.	• Read the individual information of the error using the peripheral device, check to be sure that the parameter drive name and file name correspond to the numerical values there (parameter number), and correct. • Check the space remaining in the memory card.	○
		Although setting is made to use the device data storage file, there is no empty capacity required for creating the device data storage file in the standard ROM.	Secure the empty capacity of the standard ROM.	QnU
	2402	Though the file register has been set in the pairing setting/tracking setting, the file register does not exist.	Confirm the file register and parameter.	Q4AR

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2410								
2411	FILE OPE. ERROR	File name/ Drive name	Program error location	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed	
2412								
2413								

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2410	• The specified program does not exist in the program memory. This error may occur when the ECALL, EFCALL, PSTOP, PSCAN, POFF or PLOW instruction is executed. • The specified file does not exist.	• Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct. Create a file created using parameters, and load it to the CPU module. • In case a specified file does not exist, write the file to a target memory and/ or check the file specified with the instruction again.	QnA Qn(H) QnPH QnPRH QnU
	2411	• The file is the one which cannot be specified by the sequence program (such as comment file). • The specified program exists in the program memory, but has not been registered in the program setting of the Parameter dialog box. This error may occur when the ECALL, EFCALL, PSTOP, PSCAN or POFF instruction is executed.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct.	
	2412	The SFC program file is one that cannot be designated by the sequence program.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct.	QnA Qn(H) QnPH QnPRH QnU
	2413	No data has been written to the file designated by the sequence program.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct. Check to ensure that the designated file has not been write protected.	QnA Qn(H) QnPH QnPRH

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
2500								
2501	CAN'T EXE. PRG.	File name/ Drive name	–	Off	Flicker	Stop	At power ON/ At reset	
2502								
2503								
2504								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   Either error stop or continue can be selected for each module by the parameters.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	2500	• There is a program file that uses a device that is out of the range set in the PLC parameter device setting. • After the PLC parameter setting is changed, only the parameter is written into the PLC.	• Read the common information of the error using the peripheral device, check to be sure that the parameter device allocation setting and the program file device allocation correspond to the numerical values there (file name), and correct if necessary. • If PLC parameter device setting is changed, batch-write the parameter and program file into the PLC.	○
		After the index modification of the PLC parameter is changed, only the parameter is written to the PLC.	When the index modification of the PLC parameter is changed, batch-write the parameter and program file into the PLC.	QnU
	2501	There are multiple program files although "none" has been set at the PLC parameter program settings.	Edit the PLC parameter program setting to "yes". Alternatively, delete unneeded programs.	QnA Qn(H) QnPH QnPRH QnU
		• There are three or more program files. • The program name differs from the program contents.	• Delete unnecessary program files. • Match the program name with the program contents.	Q00J/Q00/Q01
	2502	The program file is incorrect. Alternatively, the file contents are not those of a sequence program.	Check whether the program version is ∗∗∗.QPG, and check the file contents to be sure they are for a sequence program.	○
		The program file is not the one for the redundant CPU.	Create a program using GX Developer or PX Developer for which the PLC type has been set to the redundant CPU (Q12PRH/Q25PRH), and write it to the CPU module.	QnPRH
	2503	There are no program files at all.		○
	2504	Two or more SFC normal programs or control programs have been designated.	• Check program configuration. • Check parameters and program configuration.	QnA Qn(H) QnPH QnPRH QnU
		There are two or more SFC programs.	Reduce the SFC programs to one.	Q00J/Q00/Q01[*3]

*3   The function version is B or later.
*4   The function version is A.
*5   The module whose first 5 digits of serial No. is 04101 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
3000	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN/ At PLC writing	
3001								

*1 The function version is B or later.
*2 Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3000	In a multiple CPU system, the intelligent function module under control of another CPU is specified in the interrupt pointer setting of the PLC parameter.	• Specify the head I/O number of the intelligent function module under control of the host CPU. • Delete the interrupt pointer setting of the parameter.	Qn(H)[*1] QnPH QnU
		The PLC parameter settings for timer time limit setting, the RUN-PAUSE contact, the common pointer number, general data processing, number of empty slots, system interrupt settings, baud rate setting, and service processing setting are outside the range that can be used by the CPU module.	• Read the individual information of the error using the peripheral device, check the parameter item corresponding to the numerical value (parameter No.), and correct it. • Rewrite corrected parameters to the CPU module, reload the CPU power supply and/or reset the module. • If the same error occurs, it is thought to be a hardware error. (Contact your local Mitsubishi representative.)	○ Rem
		In a program memory check, the check capacity has not been set within the range applicable for the CPU module.		QnPH QnPRH[*4]
		The parameter settings in the error individual information (special register SD16) are illegal.		○
		The ATA card is set to the PLC card slot when the specified drive for the file register is set to "memory card ROM" and [Use the following file] or [Use the same file name as the program] (either one is allowed) is set in the PLC file setting.		QnU
	3001	The parameter settings are corrupted.		○ Rem

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3002	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN/ At PLC writing	
3003	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed	
	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3004	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3002	When "Use the following file" is selected for the file register in the PLC file setting of the PLC parameter dialog box, the specified file does not exist although the file register capacity has been set.	• Read the individual information of the error using the peripheral device, check the parameter item corresponding to the numerical value (parameter No.), and correct it. • Rewrite corrected parameters to the CPU module, reload the CPU power supply and/or reset the module. • If the same error occurs, it is thought to be a hardware error. (Contact your local Mitsubishi representative.)	QnA Qn(H) QnPH QnPRH
		When [Use the following file] is set for the file register in the PLC file setting of the PLC parameter dialog box and the capacity of file register is not set, the file register file does not exist in the specified target memory.		QnU
		When [Use the following file.] is set for the device data storage file in [PLC file] of [PLC parameter], and [Capacity] is not set, the device data storage file does not exist in the target memory.		QnU
		The automatic refresh range of the multiple CPU system exceeded the file register capacity.	Change the file register file for the one refresh-enabled in the whole range.	Qn(H)[*1] QnPH QnU
	3003	The number of devices set at the PLC parameter device settings exceeds the possible CPU module range.	• Read the individual information of the error using the peripheral device, check the parameter item corresponding to the numerical value (parameter No.), and correct it. • If the error is still generated following the correction of the parameter settings, the possible cause is the memory errorm of the CPU module's built-in RAM or program memory or the memory card. (Contact your local Mitsubishi representative.)	○
	3004	The parameter file is incorrect. Alternatively, the contents of the file are not parameters.	Check whether the parameter file version is ∗ ∗ ∗.QPA, and check the file contents to be sure they are parameters.	○

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3005	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-ON/ At reset/ STOP→RUN	
3006	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3007								
3009								

*1 The function version is B or later.
*2 Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ₕ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3005	The contents of the parameter are broken.	• Read the individual information of the error using the peripheral device, check the parameter item corresponding to the numerical value (parameter No.), and correct it. • Write the modified parameter items to the CPU module again, and power-on the PLC or reset the CPU module. • When the same error occurs again, the hardware is faulty. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	Qn(H)[*6]
	3006	• The high speed interrupt is set in a Q02CPU. • The high speed interrupt is set in a multiple CPU system. • The high speed interrupt is set when aQA1S6□B or QA6□B is used. • No module is installed at the I/O address designated by the high speed interrupt.	• Delete the setting of the Q02CPU' s high speed interrupt. To use high speed interrupts, change the CPU module to one of the Q02H/Q06H/Q12H/Q25HCPU. • To use a multiple CPU system, delete the setting of the high-speed interrupt. To use high speed interrupts, change the system to a single CPU system. • To use either the QA1S6□B or QA6□B, delete the setting of the high speed interrupt. To use high speed interrupts, do not use the QA1S6□B/QA6□B. • Re-examine the I/O address designated by the high speed interrupt setting.	Qn(H)[*3]
	3007	The parameter file in the drive specified as valid parameter drive by the DIP switches is inapplicable for the CPU module.	Create parameters using GX Developer, and write them to the drive specified as valid parameter drive by the DIP switches.	QnPRH
	3009	In a multiple CPU system, the modules for AnS, A, Q2AS and QnA have been set to multiple control CPUs.	Re-set the parameter I/O assignment to control them under one CPU module. (Change the parameters of all CPUs in the multiple CPU system.)	Qn(H)[*1]

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3010 3012	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3013	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10н.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3010	The parameter-set number of CPU modules differs from the actual number in a multiple CPU system.	Match the number of (CPU modules in multiple CPU setting) - (CPUs set as empty in I/O assignment) with that of actually mounted CPU modules.	Qn(H)[*1] QnPH
	3012	Multiple CPU setting or control CPU setting differs from that of the reference CPU settings in a multiple CPU system.	Match the multiple CPU setting or control CPU setting in the PLC parameter with that of the reference CPU (CPU No.1) settings.	Q00/Q01[*1] Qn(H)[*1] QnU
	3013	Multiple CPU auto refresh setting is any of the followings in a multiple CPU system. • When a bit device is specified as a refresh device, a number other than a multiple of 16 is specified for the refresh-starting device. • The device specified is other than the one that may be specified. • The number of send points is an odd number.	Check the following in the multiple CPU auto refresh setting and make correction. • When specifying the bit device, specify a multiple of 16 for the refresh starting device. • Specify the device that may be specified for the refresh device. • Set the number of send points to an even number.	Qn(H)[*1] QnPH
		In a multiple CPU system, the multiple CPU auto refresh setting is any of the following. • The total number of transmission points is greater than the maximum number of refresh points.	Check the following in the multiple CPU auto refresh setting and make correction. • The total number of transmission points is within the maximum number of refresh points.	Q00/Q01[*1]
		In a multiple CPU system, the multiple CPU auto refresh setting is any of the following. • The device specified is other than the one that may be specified. • The number of send points is an odd number. • The total number of send points is greater than the maximum number of refresh points.	Check the following in the multiple CPU auto refresh setting and make correction. • Specify the device that may be specified for the refresh device. • Set the number of send points to an even number. • Set the total number of send points within the range of the maximum number of refresh points.	QnU

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3014	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3015	PARAMETER ERROR	File name/ Drive name	Parameter number/ CPU No.	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3016	PARAMETER ERROR	File name/ Drive name	Parameter number/ CPU No.	Off	Flicker	Stop	At power ON/ At reset/ At PLC writing	
3040 3041	PARAMETER ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3014	• In a multiple CPU system, the online module change parameter (multiple CPU system parameter) settings differ from those of the reference CPU. • In a multiple CPU system, the online module change setting is enabled although the CPU module mounted does not support online module chang parameter.	• Match the online module change parameter with that of the reference CPU. • If the CPU module that does not support online module change is mounted, replace it with the CPU module that supports online module change.	Qn(H) QnPH QnU
	3015	In a multiple CPU system configuration, the CPU verified is different from the one set in the parameter setting.	Read the individual information of the error using the peripheral device, check the parameter item corresponding to the numerical value (parameter No./CPU No.) and parameter of target CPU, and correct them.	QnU
	3016	The CPU module incompatible with multiple CPU synchronized boot-up is set as the target for the synchronized boot-up in the [Multiple CPU synchronous startup setting].	Delete the CPU module incompatible with multiple CPU synchronized boot-up from the setting.	QnU[*7]
	3040	The parameter file is damaged.	With GX Developer, write [PLC parameter/network parameter/remote password] to a valid drive then reload the power supply for system and/or reset the CPU module. If the same error occurs, it is thought to be hardware error. (Contact your local Mitsubishi representative.)	Qn(H)[*4] QnPH[*4] QnPRH[*4]
	3041	Parameter file of intelligent function module is damaged.	With GX Developer, write [Intelligent function module parameter] to a valid drive to write the parameters then reload the power supply for system and/or reset the CPU module. If the same error occurs, it is thought to be a hardware error. (Contact your local Mitsubishi representative.)	Qn(H)[*4] QnPH[*4] QnPRH[*4]

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
3042	PARAMETER ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset
3100	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-ON/ At reset/ STOP→RUN

*1 The function version is B or later.
*2 Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3042	The system file that have stored the remote password setting information is damaged.	• With GX Developer, write [PLC parameter/network parameter/remote password] to a valid drive then reload the power supply for system and/or reset the CPU module. If the same error occurs, it is thought to be a hardware error. (Contact your local Mitsubishi representative.) • When a valid drive for parameter is set to other than [program memory], set the parameter file (PARAM) at the boot file setting to be able to transmit to the program memory. With GX Developer, write [PLC parameter/netwark parameter/remote password] to a valid drive then reload the power supply for system and/or reset the CPU module. If the same error occurs, it is thought to be hardware error. (Contact your local Mitsubishi representative.)	Qn(H)[*4] QnPH[*4] QnPRH[*4]
	3100	In a multiple CPU system, the MELSECNET/G module controlled by another CPU is specified as the head I/O number of the MELSECNET/G module.	• Delete the net work parameter of the MELSECNET/G module controlled by another CPU. • Change the setting to the head I/O number of the MELSECNET/G module controlled by host CPU.	Qn(H)[*6] QnU
		The network parameter of the MELSECNET/G operating as the normal station is overwritten to the control station. Or, the network parameter of the MELSECNET/G operating as the control station is overwritten to the normal station. (The network parameter is updated on the module by resetting.)	Reset the CPU module.	

*3　The module whose first 5 digits of serial No. is 04012 or later.
*4　The module whose first 5 digits of serial No. is 07032 or later.
*5　The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6　The module whose first 5 digits of serial No. is 09012 or later.
*7　The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
3100	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-ON/ At reset/ STOP→RUN	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ᴴ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3100	• The number of actually mounted modules is different from the number of modules set in the network parameter of the MELSECNET/G. • The head I/O number of the actually mounted module is different from the one set in the network parameter of the MELSECNET/G. • Data cannot be handled in the parameter exists. • The network type of MELSECNET/G is overwritten during power-on. (When changing the network type, switch RESET to RUN.) • The MELSECNET/G module is specified for the head I/O number of network parameter in the MELSECNET/H. • The MELSECNET/H module is specified for the head I/O number of network parameter in the MELSECNET/G. • Although the MELSECNET/G module is mounted, network parameter for the MELSECNET/G module is not set. • Although the MELSECNET/G and MELSECNET/H modules are mounted, network parameter for the MELSECNET/H module is not set.	• Check the network parameter and actual mounting status, and if they differ, make them matched. When network parameters are modified, write them to the CPU module. • Check the setting of extension base unit stage number. • Check the connection status of extension base unit and extension cable. When the GOT is bus-connected to the main base unit or extension base unit, also check its connection status. If an error occurs even after performing the above checks, the hardware may be faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Qn(H)[*6] QnU

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
3100	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 The function version is B or later.
*2 Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10н.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		In a multiple CPU system, the MELSECNET/H under control of another CPU is specified as the head I/O number in the network setting parameter of the MELSECNET/H.	• Delete the MELSECNET/H network parameter of the MELSECNET/H under control of another CPU. • Change the setting to the head I/O number of the MELSECNET/H under control of the host CPU.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
		The network parameter of the MELSECNET/H operating as the normal station is overwritten to the control station. Or, the network parameter of the MELSECNET/H operating as the control station is overwritten to the normal station. (The network parameter is updated on the module by resetting.)	Reset the CPU module.	Qn(H)[*1] QnPH QnPRH QnU
	3100	• The number of actually installed modules is different from that designated in the number of modules setting parameter of MELSECNET/H. • The head I/O number of actually installed modules is different from that designated in the network parameter of MELSECNET/H. • Some data in the parameters cannot be handled. • The network type of MELSECNET/H is overwritten during power-on. (When changing the network type, switch RESET to RUN.) • The mode switch of MELSECNET/H module[*5] is outside the range.	• Check the network parameters and actual mounting status, and if they differ, make them matched. If any network parameter has been corrected, write it to the CPU module. • Check the extension base unit stage No. setting. • Check the connection status of the extension base units and extension cables. When the GOT is bus-connected to the main base unit and extension base units, also check the connection status. If the error occurs after the above checks, the possible cause is a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Set the mode switch of MELSECNET/H module[*5] within the range.	QCPU
		Although the QnACPU is a control station or master station, the network parameters have not been written.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	QnA

*3　The module whose first 5 digits of serial No. is 04012 or later.
*4　The module whose first 5 digits of serial No. is 07032 or later.
*5　The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6　The module whose first 5 digits of serial No. is 09012 or later.
*7　The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed	
3101	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ₕ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		The link refresh range exceeded the file register capacity.	Change the file register file for the one that enables entire range refresh.	Qn(H)[*1] QnPH QnPRH QnU
		• When the station number of the MELSECNET/H module is 0, the inter-PLC network parameter setting has been made. • When the station number of the MELSECNET/H module is other than 0, the remote master parameter setting has been made.	Correct the type or station number of the MELSECNET/H module in the network parameter to meet the used system.	Qn(H)[*1] QnPH QnPRH
	3101	The refresh parameter for the MELSECNET/G is outside the range.	• Check the network parameters and mounting status, and if they differ, match the network parameters and mounting status. If any network parameter has been corrected, write it to the CPU module. • Confirm the setting of the number of extension stages of the extension base units. • Check the connection status of the extension base units and extension cables. When the GOT is bus-connected to the main base unit and extension base units, also check their connection status. If the error occurs after the above checks, the cause is a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Qn(H)[*6] QnU
		• The network No. specified by a network parameter is different from that of the actually mounted network. • The head I/O No. specified by a network parameter is different from that of the actually mounted I/O unit. • The network class specified by a network parameter is different from that of the actually mounted network. • The network refresh parameter of the MELSECNET/H, MELSECNET/10 is out of the specified area.		○
		A multi-remote I/O network was configured using a module that does not support the MELSECNET/H multi-remote I/O network.	Use a module that supports the MELSECNET/H multi-remote I/O network.	QnPH
		• The system A of the MELSECNET/H remote master station has been set to other than Station No. 0. • The system B of the MELSECNET/H remote master station has been set to Station No. 0.	• Set the system A of the MELSECNET/H remote master station to Station No. 0. • Set the system B of the MELSECNET/H remote master station to any of Station No. 1 to 64.	QnPRH

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3102	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3102	A MELSECNET/G network parameter error was detected.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	Qn(H)[6] QnU
		• The network module detected a network parameter error. • A MELSECNET/H network parameter error was detected.		○
		The station No. specified in pairing setting are not correct. • The stations are not numbered consecutively. • Pairing setting has not been made for the CPU module at the normal station.	Refer to the troubleshooting of the network module, and if the error is due to incorrect pairing setting, reexamine the pairing setting of the network parameter.	QnPRH
		The MELSECNET/G module whose first 5 digits of serial No. is "09041" or earlier is mounted.	Mount the MELSECNET/G module whose first 5 digits of serial No. is "09042" or later.	QnU

*3 The module whose first 5 digits of serial No. is 04012 or later.
*4 The module whose first 5 digits of serial No. is 07032 or later.
*5 The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
3103	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3103	In a multiple CPU system, the Q series Ethernet interface module under control of another station is specified to the start I/O number of the Ethernet network parameter.	• Delete the Ethernet network parameter of the Q series Ethernet interface module under control of another station. • Change the setting to the start I/O number of the Q series Ethernet interface module under control of the host station.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
		• Although the number of modules has been set to one or greater number in the Ethernet module count parameter setting, the number of actually mounted module is zero. • The start I/O No. of the Ethernet network parameter differs from the I/O No. of the actually mounted module.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
		• AJ71QE71 does not exist in the position of I/O number set by the parameter. • I/O number designation is overlapping. • Numbers of the network parameter and loaded AJ71QE71 are different. • Ethernet (parameter + dedicated instruction) is set to more than five.		QnA
		• Ethernet module whose network type is set to "Ethernet (main base)" is mounted on the extension base unit in the redundant system. • Ethernet module whose network type is set to "Ethernet (extension base)" is mounted on the main base unit in the redundant system.		QnPRH[*6]

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
3104								
3105	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ₕ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3104	• The Ethernet, MELSECNET/H and MELSECNET/10 use the same network number. • The network number, station number or group number set in the network parameter is out of range. • The specified I/O number is outside the range of the used CPU module. • The Ethernet-specific parameter setting is not normal.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
	3105	In a multiple CPU system, the Q series CC-Link module under control of another station is specified as the head I/O number of the CC-Link network parameter.	• Delete the CC-Link network parameter of the Q series CC-Link module under control of another station. • Change the setting to the start I/O number of the Q series CC-Link module under control of the host station.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
		• Though the number of CC-Link modules set in the network parameters is one or more, the number of actually mounted modules is zero. • The start I/O number in the common parameters is different from that of the actually mounted module. • The station type of the CC-Link module count setting parameters is different from that of the actually mounted station.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
		• CC-Link module whose station type is set to "master station (compatible with redundant function)" is mounted on the extension base unit in the redundant system. • CC-Link module whose station type is set to "master station" is mounted on the main base unit in the redundant system.		QnPRH[*6]
		The contents of the Ethernet parameter are incorrect.	Write after correcting parameters.	QnA

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3106	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed	
	LINK PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3107	LINK PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3200	SFC PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	STOP→RUN	
3201								
3202								
3203								
3300	SP. PARA ERROR	File name	Parameter number^{*2}	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3301								
3302								

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10H.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3106	The CC-Link link refresh range exceeded the file register capacity.	Change the file register file for the one refresh-enabled in the whole range.	Qn(H)[*1] QnPH QnPRH QnU
		The network refresh parameter for CC-Link is out of range.	Check the parameter setting.	QCPU Rem
	3107	• The CC-Link parameter setting is incorrect. • The set mode is not allowed for the version of the mounted CC-Link module.	Check the parameter setting.	○ Rem
	3200	The parameter setting is illegal. • Though Block 0 was set to "Automatic start" in the SFC setting of the PLC parameter dialog box, Block 0 does not exist.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Q00J/Q00/ Q01[*1] QnPH QnPRH QnU
	3201	The block parameter setting is illegal.		QnA Qn(H) QnPH QnPRH
	3202	The number of step relays specified in the device setting of the PLC parameter dialog box is less than that used in the program.		
	3203	The execution type of the SFC program specified in the program setting of the PLC parameter dialog box is other than scan execution.		QnA Qn(H) QnPH QnPRH QnU
	3300	The start I/O number in the intelligent function module parameter set on GX Configurator differs from the actual I/O number.	Check the parameter setting.	QCPU Rem
	3301	The refresh setting of the intelligent function module exceeded the file register capacity.	Change the file register file for the one which allows refresh in the whole range.	Q00J/Q00/Q01 Qn(H)[*1] QnPH QnPRH QnU
		The intelligent function module's refresh parameter setting is outside the available range.	Check the parameter setting.	QCPU Rem
	3302	The intelligent function module's refresh parameter are abnormal.	Check the parameter setting.	QCPU

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07032 or later.
*5   The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3303	SP. PARA ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing	
3400	REMOTE PASS. ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1   The function version is B or later.
*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ₕ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3303	In a multiple CPU system, the automatic refresh setting or other parameter setting was made to the intelligent function module under control of another station.	• Delete the automatic refresh setting or other parameter setting of the intelligent function module under control of another CPU. • Change the setting to the automatic refresh setting or other parameter setting of the intelligent function module under control of the host CPU.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
	3400	The head I/O number of the target module of the remote password is set to other than 0_H to 0FF0$_H$.	Change the head I/O number of the target module to be within the 0_H to 0FF0$_H$ range.	Qn(H)[*1] QnPH QnPRH QnU[*7] Rem
		The head I/O number of the target module of the remote password is set to other than 0_H to 07E0$_H$.	Change the head I/O number of the target module to be within the 0_H to 07E0$_H$ range.	Q02U
		The head I/O number of the target module of the remote password is outside the following range. • Q00JCPU: 0_H to 1E0$_H$ • Q00CPU/Q01CPU: 0_H to 3E0$_H$	Change the head I/O number of the target module of the remote password for the number within the following range. • Q00JCPU: 0_H to 1E0$_H$ • Q00CPU/Q01CPU: 0_H to 3E0$_H$	Q00J/Q00/Q01[*1]

*3　The module whose first 5 digits of serial No. is 04012 or later.
*4　The module whose first 5 digits of serial No. is 07032 or later.
*5　The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6　The module whose first 5 digits of serial No. is 09012 or later.
*7　The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
3401	REMOTE PASS. ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1   The function version is B or later.

*2   Parameter No. is the value gained by dividing the head I/O number of parameter in the intelligent function module set by GX Configurator by 10ₕ.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	3401	Position specified as the head I/O number of the remote password file is incorrect due to one of the following reasons: • Module is not loaded. • Other than a the intelligent function module (I/O module) • Intelligent function module other than the Q series serial communication module, modem interface module or Ethernet module • Q series serial communication module or Ethernet module of function version A The intelligent function module where remote password is available is not mounted.	Mount the Q series serial communication module, modem interface module or Ethernet module of function version B or later in the position specified in the head I/O No. of the remote password file.	Qn(H)[*1] QnPH QnPRH QnU Rem
		Any of the following modules is not mounted on the slot specified for the head I/O number of the remote password. • Serial communication module of function version B or later • Ethernet module of function version B or later • Modem interface module of function version B or later	Mount any of the following modules in the position specified for the head I/O number of the remote password. • Serial communication module of function version B or later • Ethernet module of function version B or later • Modem interface module of function version B or later	Q00J/Q00/ Q01[*1]
		The Q series serial communication module, modem interface module or Ethernet module of function version B or later controlled by another CPU was specified in a multiple CPU system.	• Change it for the Ethernet module of function version B or later connected by the host CPU. • Delete the remote password setting.	Qn(H)[*1] QnPH QnU

*3 The module whose first 5 digits of serial No. is 04012 or later.
*4 The module whose first 5 digits of serial No. is 07032 or later.
*5 The MELSECNET/H module whose first 5 digits of serial No. is 08102 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

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| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
4000	INSTRCT. CODE ERR	Program error location	–	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN When instruction executed
4001							
4002							
4003							
4004							
4010	MISSING END INS.	Program error location	–	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN
4020	CAN'T SET(P)	Program error location	–	Off	Flicker	Stop	
4021							
4030	CAN'T SET(I)	Program error location	–	Off	Flicker	Stop	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4000	• The program contains an instruction code that cannot be decoded. • An unusable instruction is included in the program.	Read the common information of the error using a peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4001	The program contains a dedicated instruction for SFC although it is not an SFC program.		QnA Q00J/Q00/ Q01[*2] Qn(H) QnPH QnPRH QnU
	4002	• The name of dedicated instruction specified by the program is incorrect. • The dedicated instruction specified by the program cannot be executed by the specified module.		○ Rem
	4003	The number of devices for the dedicated instruction specified by the program is incorrect.		
	4004	The device which cannot be used by the dedicated instruction specified by the program is specified.		
	4010	There is no END (FEND) instruction in the program.		○
	4020	The total number of internal file pointers used by the program exceeds the number of internal file pointers set in the parameters.		QnA Qn(H) QnPH QnPRH QnU
	4021	• The common pointer Nos. assigned to files overlap. • The local pointer Nos. assigned to files overlap.		○
	4030	The allocation pointer Nos. assigned by files overlap.		

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

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| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
4100								
4101	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[1]	When instruction executed	

[1] CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
[2] The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4100	The instruction cannot process the contained data.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○ Rem
		Access error of ATA card occurs by FREAD/FWRITE instructions.	• Take measurements against noise. • Reset and restart the CPU module. When the same error is displayed again, the ATA card has hardware failure. (Please consult your local Mitsubishi service center or representative, explaining a detailed description of the problem.)	Qn(H) QnPH QnPRH
	4101	• The number of setting data dealt with the instruction exceeds the applicable range. • The storage data and constant of the device specified by the instruction exceeds the applicable range. • When writing to the host CPU shared memory, the write prohibited area is specified for the write destination address. • The range of storage data of the device specified by the instruction is duplicated. • The device specified by the instruction exceeds the range of the number of device points. • The interrupt pointer No. specified by the instruction exceeds the applicable range.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○ Rem
		• The storage data of file register specified by the instruction exceeds the applicable range. Or, file register is not set.		QnU

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
4102							
4103	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed
4104							
4105							
4107							
4108							

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4102	In a multiple CPU system, the link direct device (J□\□) was specified for the network module under control of another station.	• Delete from the program the link direct device which specifies the network module under control of another CPU. • Using the link direct device, specify the network module under control of the host CPU.	Q00/Q01[*2] Qn(H)[*2] QnPH QnU
		• The network No. or station No. specified for the dedicated instruction is wrong. • The link direct device (J□\□) setting is incorrect. • The module No./ network No./number of character strings exceeds the range that can be specified.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○ Rem
		• The specification of character string (" ") specified by dedicated instruction cannot be used for the character string.		QnU
	4103	The configuration of the PID dedicated instruction is incorrect.		QnA Q00J/Q00/Q01[*2] Qn(H) QnPRH QnU
	4104	The number of settings is beyond the range.	Read the common information of the error using peripheral device, and check and correct the program corresponding to that value (program error location).	Q4AR
	4105	PLOADP/PUNLOADP/PSWAPP instructins were executed while setting program memory check.	• Delete the program memory check setting. • When using the program memory check, delete PLOADP/PUNLOADP/ PSWAPP instructions.	QnPH[*5]
	4107	33 or more multiple CPU dedicated instructions were executed from one CPU module.	Using the multiple CPU dedicated instruction completion bit, provide interlocks to prevent one CPU module from executing 33 or more multiple CPU dedicated instructions.	Q00/Q01[*2] Qn(H)[*2] QnPH Q02U
		Numbers of execution to the CC-Link instruction are beyond 32.	Set the numbers of execution to the CC-Link instruction to 32 or less.	QnA
	4108	The CC-Link parameter is not set when the CC-Link instruction is executed.	Execute the CC-Link instruction after setting the CC-Link parameter.	

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
4109	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed	
4111 4112	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*2]	When instruction executed	
4113	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue	When instruction executed	
4120 4121	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*2]	When instruction executed	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4109	With high speed interrupt setting PR, PRC, UDCNT1, UDCNT2, PLSY or PWM instruction is executed.	Delete the high-speed interrupt setting. When using high-speed interrupt, delete the PR, PRC, UDCNT1, UDCNT2, PLSY and PWM instructions.	Qn(H)[*3]
	4111	An attempt was made to perform write/read to/from the CPU shared memory write/read disable area of the host station CPU module with the instruction.	Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	Q00/Q01[*2] QnU
	4112	The CPU module that cannot be specified with the multiple CPU dedicated instruction was specified.		
	4113	• When the SP.DEVST instruction is executed, the number of writing to the standard ROM of the day exceeds the value specified by SD695. • The value outside the specified range is set to SD695.	• Check that the number of execution of the SP.DEVST instruction is proper. • Execute the SP.DEVST instruction again the following day or later day. Or, arrange the value of SD695. • Correct the value of SD695 so that it does not exceed the range.	QnU
	4120	Since the manual system switching enable flag (special register SM1592) is OFF, manual system switching cannot be executed by the control system switching instruction (SP. CONTSW).	To execute control system switching by the SP. CONTSW instruction, turn ON the manual system switching enable flag (special register SM1592).	QnPRH
	4121	• In the separate mode, the control system switching instruction (SP. CONTSW) was executed in the standby system CPU module. • In the debug mode, the control system switching instruction (SP. CONTSW) was executed.	• Reexamine the interlock signal for the SP. CONTSW instruction, and make sure that the SP. CONTSW instruction is executed in the control system only. (Since the SP. CONTSW instruction cannot be executed in the standby system, it is recommended to provide an interlock using the operation mode signal or like.) • As the SP. CONTSW instruction cannot be executed in the debug mode, reexamine the interlock signal related to the operation mode.	

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
4122	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue	When instruction executed	
4130	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue*2	When END/ other instruction executed	
4131	OPERATION ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue	When instruction executed	
4140	OPERATION ERROR	Program error location	–	OFF/ On	Flicker/ On	Stop/ Continue*2	When instruction executed	
4141								

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 The function version is B or later.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4122	• The dedicated instruction was executed to the module mounted on the extension base unit in the redundant system. • The instruction for accessing the intelligent function module mounted on the extension base unit from the standby system at separate mode was executed.	• Delete the dedicated instruction for the module mounted on the extension base unit. • Delete the instruction for accessing the intelligent function module mounted on the extension base unit from the standby system.	QnPRH[*6]
	4130	Instructions to read SFC step comment (S(P).SFCSCOMR) and SFC transition condition comment (S(P).SFCTCOMR) are executed for the comment file in ATA card	Target comment file is to be other than the comment file in ATA card.	Qn(H)[*4] QnPH[*5] QnPRH
	4131	The SFC program is started up by the instruction while the other SFC program has not yet been completed.	Check the SFC program specified by the instruction. Or, check the executing status of the SFC program.	QnU
	4140	Operation where the input data is special value ("-0", unnormalized number, nonnumeric, $\pm \infty$) is performed.	Read the common information of the error using the peripheral device, check the error step corresponding to the numerical value (program error part), and correct it.	QnU
	4141	Overflow occurs at operation.		QnU

*3 The module whose first 5 digits of serial No. is 04012 or later.
*4 The module whose first 5 digits of serial No. is 07012 or later.
*5 The module whose first 5 digits of serial No. is 07032 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
4200	FOR NEXT ERROR	Program error location	–	Off	Flicker	Stop	When instruction executed	
4201	FOR NEXT ERROR	Program error location	–	Off	Flicker	Stop	When instruction executed	
4202								
4203								
4210	CAN'T EXECUTE(P)	Program error location	–	Off	Flicker	Stop	When instruction executed	
4211								
4212								
4213								
4220	CAN'T EXECUTE(I)	Program error location	–	Off	Flicker	Stop	When instruction executed	
4221								
4223								
4225	CAN'T EXECUTE(I)	–	–	Off	Flicker	Stop	At power-ON/ At reset	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4200	No NEXT instruction was executed following the execution of a FOR instruction. Alternatively, there are fewer NEXT instructions than FOR instructions.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4201	A NEXT instruction was executed although no FOR instruction has been executed. Alternatively, there are more NEXT instructions than FOR instructions.		
	4202	More than 16 nesting levels are programmed.	Keep nesting levels at 16 or under.	
	4203	A BREAK instruction was executed although no FOR instruction has been executed prior to that.		○
	4210	The CALL instruction is executed, but there is no subroutine at the specified pointer.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4211	There was no RET instruction in the executed subroutine program.		
	4212	The RET instruction exists before the FEND instruction of the main routine program.		
	4213	More than 16 nesting levels are programmed.	Keep nesting levels at 16 or under.	
	4220	Though an interrupt input occurred, the corresponding interrupt pointer does not exist.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4221	An IRET instruction does not exist in the executed interrupt program.		
		The IRET instruction exists before the FEND instruction of the main routine program.		
	4223	• The IRET instruction was executed in the fixed scan execution type program. • The STOP instruction was executed in the fixed scan execution type program.		QnU
	4225	The interrupt pointer for the module mounted on the extension base unit is set in the redundant system.	Delete the setting of interrupt pointer for the module mounted on the extension base unit, since it cannot be used.	QnPRH[*6]

*3 The module whose first 5 digits of serial No. is 04012 or later.
*4 The module whose first 5 digits of serial No. is 07012 or later.
*5 The module whose first 5 digits of serial No. is 07032 or later.
*6 The module whose first 5 digits of serial No. is 09012 or later.
*7 The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
4230	INST. FORMAT ERR.	Program error location	–	Off	Flicker	Stop	When instruction executed	
4231								
4235								
4300	EXTEND INST. ERR.	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed	
4301								

*1 CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2 The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4230	The number of CHK and CHKEND instructions is not equal.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH
	4231	The number of IX and IXEND instructions is not equal.		○
	4235	The configuration of the check conditions for the CHK instruction is incorrect. Alternatively, a CHK instruction has been used in a low speed execution type program.		QnA Qn(H) QnPH
	4300	The designation of a MELSECNET/ MINI-S3 master module control instruction was wrong.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA
	4301	The designation of an AD57/AD58 control instruction was wrong.		

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
4350								
4351	OPERATION ERROR	Program error location	–	OFF	Flicker	Stop	When instruction executed	
4352								
4353								
4354								
4355								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4350	• The target CPU module specified by the dedicated instruction of Multiple CPU high speed bus specified by the program. • The reserved CPU is specified. • The uninstalled CPU is specified. • The head I/O number of the target CPU/16 (n1) is outside the range of 3EH to 3E3H. • The CPU module where the instruction cannot be executed is specified. • The instruction is executed in a single CPU system. • The host CPU is specified.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnU
	4351	• The dedicated instruction of Multiple CPU high speed bus specified by the program cannot be executed to the specified target CPU module. • The instruction name is wrong. • The instruction unsupported by the target CPU module is specified.		QnU[*7]
	4352	The number of devices for the dedicated instruction of Multiple CPU high speed bus specified by the program is wrong.		
	4353	The device which cannot be used for the dedicated instruction of Multiple CPU high speed bus specified by the program is specified.		QnU
	4354	The character string which cannot be handled by the dedicated instruction of Multiple CPU high speed bus is specified.		QnU[*7]
	4355	The number of read/write data (number of request/receive data) for the dedicated instruction of Multiple CPU high speed bus specified by the program is not valid.		

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
4400	SFCP. CODE ERROR	Program error location	–	Off	Flicker	Stop	STOP→RUN	
4410 4411	CAN'T SET(BL)	Program error location	–	Off	Flicker	Stop	STOP→RUN	
4420	CAN'T SET(S)	Program error location	–	Off	Flicker	Stop	STOP→RUN	
4421 4422 4423	CAN'T SET(S)	Program error location	–	Off	Flicker	Stop	STOP→RUN	
4430 4431 4432	SFC EXE. ERROR	File name/ Drive name	–	Off	Flicker	Stop	STOP→RUN	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4400	No SFCP or SFCPEND instruction in SFC program.	Write the program to the CPU module again using GX Developer.	QnA Qn(H) QnPH QnPRH
	4410	The block number designated by the SFC program exceeds the range.		QnA Q00J/Q00/ Q01[*2] Qn(H) QnPH QnPRH QnU
	4411	Block number designations overlap in SFC program.		
	4420	A step number designated in an SFC program exceeds the range.		
	4421	Total number of steps in all SFC programs exceed the maximum.		QnA Q00J/Q00/ Q01[*2] Qn(H) QnPH QnPRH QnU
	4422	Step number designations overlap in SFC program.		
	4423	The total number of (maximum step No.+1) of each block exceeds the total number of step relays.	Correct the total number of step relays so that it does not exceed the total number of (maximum step No.+1) of each block.	
	4430	The SFC program cannot be executed. • The data of the block data setting is illegal. • The SFC data device of the block data setting is beyond the device setting range set in the PLC parameter.	• Write the program to the CPU module again using GX Developer. • After correcting the setting of the SFC data device, write it to the CPU module. • After correcting the device setting range set in the PLC parameter, write it to the CPU module.	Q00J/Q00/ Q01[*2] QnU
	4431	The SFC program cannot be executed. • The block parameter setting is abnormal.	Write the program to the CPU module again using GX Developer.	
	4432	The SFC program cannot be executed. • The structure of the SFC program is illegal.		

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
4500								
4501								
4502								
4503	SFCP. FORMAT ERR.	Program error location	–	Off	Flicker	Stop	STOP→RUN	
4504								
4505								
4506								
4600								
4601	SFCP. OPE. ERROR	Program error location	–	Off/ On	Flicker/ On	Stop/ Continue[*1]	When instruction executed	
4602								

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4500	The numbers of BLOCK and BEND instructions in an SFC program are not equal.	Write the program to the CPU module again using the peripheral device.	QnA Qn(H) QnPH QnPRH
	4501	The configuration of the STEP* to TRAN* to TSET to SEND instructions in the SFC program is incorrect.		
	4502	The structure of the SFC program is illegal. • STEPI* instruction does not exist in the block of the SFC program.		QnA Q00J/Q00/ Q01[*2] Qn(H) QnPH QnPRH QnU
	4503	The structure of the SFC program is illegal. • The step specified in the TSET instruction does not exist. • In jump transition, the host step number was specified as the destination step number.	• Write the program to the CPU module again using GX Developer. • Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	
	4504	The structure of the SFC program is illegal. • The step specified in the TAND instruction does not exist.	Write the program to the CPU module again using GX Developer.	
	4505	The structure of the SFC program is illegal. • In the operation output of a step, the SET Sn/BLmSn or RST Sn/BLmSn instruction was specified for the host step.	Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	Q00J/Q00/ Q01[*2] QnU
	4506	The structure of the SFC program is illegal. • In a reset step, the host step number was specified as the destination step.		
	4600	The SFC program contains data that cannot be processed.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4601	Exceeds device range that can be designated by the SFC program.		
	4602	The START instruction in an SFC program is preceded by an END instruction.		

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
4610 4611	SFCP. EXE. ERROR	Program error location	–	On	On	Continue	STOP→RUN	
4620 4621	BLOCK EXE. ERROR	Program error location	–	Off	Flicker	Stop	When instruction executed	
4630 4631 4632 4633	STEP EXE. ERROR	Program error location	–	Off	Flicker	Stop	When instruction executed	

*1   CPU operation can be set in the parameters at error occurrence. (LED indication varies.)
*2   The function version is B or later.

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	4610	The active step information at presumptive start of an SFC program is incorrect.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem. The program is automatically subjected to an initial start.	QnA Qn(H) QnPH QnPRH
	4611	Key-switch was reset during RUN when presumptive start was designated for SFC program.		
	4620	Startup was executed at a block in the SFC program that was already started up.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4621	Startup was attempted at a block that does not exist in the SFC program.	• Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location). • Turn ON if the special relay SM321 is OFF.	QnA Q00J/Q00/ Q01*2 Qn(H) QnPH QnPRH QnU
	4630	Startup was executed at a block in the SFC program that was already started up.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4631	• Startup was attempted at the step that does not exist in the SFC program. Or, the step that does not exist in the SFC program was specified for end. • Forced transition was executed based on the transition condition that does not exit in the SFC program. Or, the transition condition for forced transition that does not exit in the SFC program was canceled.	• Read the common information of the error using the peripheral device, and check and correct the error step corresponding to that value (program error location). • Turn ON if the special relay SM321 is OFF.	QnA Q00J/Q00/ Q01*2 Qn(H) QnPH QnPRH QnU
	4632	There were too many simultaneous active steps in blocks that can be designated by the SFC program.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH QnU
	4633	There were too many simultaneous active steps in all blocks that can be designated.		

*3   The module whose first 5 digits of serial No. is 04012 or later.
*4   The module whose first 5 digits of serial No. is 07012 or later.
*5   The module whose first 5 digits of serial No. is 07032 or later.
*6   The module whose first 5 digits of serial No. is 09012 or later.
*7   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
5000	WDT ERROR	Time (value set)	Time (value actually measured)	Off	Flicker	Stop	Always	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	5000	• The scan time of the initial execution type program exceeded the initial execution monitoring time specified in the PLC RAS setting of the PLC parameter.	• Read the individual information of the error from the peripheral device, check its value (time), and shorten the scan time. • Change the initial execution monitoring time or the WDT value in the PLC RAS setting of the PLC parameter. • Resolve the endless loop caused by jump transition.	QnA Qn(H) QnPH QnPRH QnU
		• The power supply of the standby system is turned OFF. • The tracking cable is disconnected or connected without turning off or resetting the standby system. • The tracking cable is not secured by the connector fixing screws.	• Since power-off of the standby system increases the control system scan time, reset the WDT value, taking the increase of the control system scan time into consideration. • When the tracking cable is disconnected during operation, securely connect it and restart the CPU module. If the same error is displayed again, the tracking cable or CPU module has a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH

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| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
5001	WDT ERROR	Time (value set)	Time (value actually measured)	Off	Flicker	Stop	Always	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	5001	• The scan time of the program exceeded the WDT value specified in the PLC RAS setting of the PLC parameter.	• Read the individual information of the error using the peripheral device, check its value (time), and shorten the scan time. • Change the initial execution monitoring time or the WDT value in the PLC RAS setting of the PLC parameter. • Resolve the endless loop caused by jump transition.	○
		• The power supply of the standby system is turned OFF. • The tracking cable is disconnected or connected without turning off or resetting the standby system. • The tracking cable is not secured by the connector fixing screws.	• Since power-off of the standby system increases the control system scan time, reset the WDT value, taking the increase of the control system scan time into consideration. • When the tracking cable is disconnected during operation, securely connect it and restart the CPU module. If the same error is displayed again, the tracking cable or CPU module has a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH

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| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
5010	PRG. TIME OVER	Time (value set)	Time (value actually measured)	On	On	Continue	Always	
5011								

Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
5010	The program scan time exceeded the constant scan setting time specified in the PLC RAS setting of the PLC parameter.	• Review the constant scan setting time. • Review the constant scan setting time and low speed program execution time in the PLC parameter so that the excess time of constant scan can be fully secured.	QnA Qn(H) QnPH QnPRH QnU
	The low speed program execution time specified in the PLC RAS setting of the PLC parameter exceeded the excess time of the constant scan.		QnA Qn(H) QnPH QnPRH
	The program scan time exceeded the constant scan setting time specified in the PLC RAS setting of the PLC parameter.	Review the constant scan setting time in the PLC parameter so that the excess time of constant scan can be fully secured.	Q00J/Q00/Q01
5011	The scan time of the low speed execution type program exceeded the low speed execution watch time specified in the PLC RAS setting of the PLC parameter dialog box.	Read the individual information of the error using the peripheral device, check the numerical value (time) there, and shorten scan time if necessary. Change the low speed execution watch time in the PLC RAS setting of the PLC parameter dialog box.	QnA Qn(H) QnPH

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
6000	PRG. VERIFY ERR.	File name	–	Off	Flicker	Stop	Always
	FILE DIFF.	File name	–	Off	Flicker	Stop	At power ON/ At reset/ At tracking cable connection/ At changing to backup mode/ At completion of write during RUN/ At system switching/ At switching both systems into RUN

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		The control system and standby system in the redundant system do not have the same programs and parameters. (This can be detected from the standby system of the redundant system.)	Synchronise the programs and parameters of the control system and standby system.	Q4AR

	6000	In a redundant system, the control system and standby system do not have the same programs and parameters.
		
		The file type detected as different between the two systems can be checked by the file name of the error common information.
		 • The program is different.
		  (File name = ********.QPG)
		 • The PLC parameters/network parameters/redundant parameters are different.
		  (File name = PARAM.QPA)
		 • The remote password is different.
		  (File name = PARAM.QPA)
		 • The intelligent function module parameters are different.
		  (File name = IPARAM.QPA)
		 • The device initial values are different.
		  (File name = ********.QDI)
		 • The capacity of each write destination within the CPU for online pchange of multiple program blocks is different.
		  (File name = MBOC.QMB)
		(This can be detected from the standby system of the redundant system.)
			• Match the programs and parameters of the control system and standby system.
			• Perform PLC verify in either of the following procedures 1), 2) to clarify the differences between the files of the two systems, then correct the wrong file, and write the corrected file to the PLC again.
			 1) After reading the programs/ parameters of System A using GX Developer or PX Developer, verify them with those of System B.
			 2) Verify the programs/parameters of GX Developer or PX Developer saved in the offline environment with those written to the CPU modules of both systems.
			• When the capacity of each write destination within the CPU for online change of multiple program blocks is different between the two systems, take corrective action 1) or 2).
			 1) Using the memory copy from control system to standby system, copy the program memory from the control system to the standby system.
			 2) Format the CPU module program memories of both systems. (For the capacity of each write destination within the CPU for online change of multiple program blocks, set the same value to both systems.)
				QnPRH

*1   The module whose first 5 digits of serial No. is 09012 or later.

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| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing |
				RUN	ERROR		
6001	FILE DIFF.	–	–	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection/At operation mode change
6010	MODE. VERIFY ERR.	–	–	On	On	Continue	Always
	OPE. MODE DIFF.	–	–	On	On	Continue	Always
6020	OPE. MODE DIFF.	–	–	Off	Flicker	Stop	At power ON/ At reset
6030	UNIT LAY. DIFF.	Module No.	–	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection/At operation mode change
6035	UNIT LAY. DIFF.	–	–	Off	Flicker	Stop	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6001	In a redundant system, the valid parameter drive settings (SW2, SW3) made by the DIP switches are not the same.	Match the valid parameter drive settings (SW2, SW3) by the DIP switches of the control system and standby system.	QnPRH
	6010	The operational status of the control system and standby system in the redundant system is not the same. (This can be detected from the standby system of the redundant system.)	Synchronise the operation statuses of the control system and standby system.	Q4AR
		The operational status of the control system and standby system in the redundant system is not the same. (This can be detected from the standby system of the redundant system.)	Synchronise the operation statuses of the control system and standby system.	
	6020	At power ON/reset, the RUN/STOP switch settings of the control system and standby system are not the same in a redundant system. (This can be detected from the control system or standby system of the redundant system.)	Set the RUN/STOP switches of the control system and standby system to the same setting.	
	6030	• In a redundant system, the module configuration differs between the control system and standby system. • The network module mode setting differs between the two systems. (This can be detected from the control system or standby system of the redundant system.)	• Match the module configurations of the control system and standby system. • In the redundant setting of the network parameter dialog box, match the mode setting of System B to that of System A.	QnPRH
	6035	In a redundant system, the CPU module model name differs between the control system and standby system. (This can be detected from the standby system of the redundant system.)	Match the model names of the control system and standby system.	

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6036	UNIT LAY. DIFF.	Module No.	–	Off	Flicker	Stop	Always	
6040	CARD TYPE DIFF.	–	–	Off	Flicker	Stop	At power ON/ At reset	
6041		–	–	Off	Flicker	Stop	At power ON/ At reset	
6050	CAN'T EXE. MODE	–	–	On	On	Continue	Always	
6060	CPU MODE DIFF.	–	–	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection	
6061	CPU MODE DIFF.	–	–	Off	Flicker	Stop	When an END instruction executed	
6062	CPU MODE DIFF.	–	–	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6036	A difference in the remote I/O configuration of the MELSECNET/H multiplexed remote I/O network between the control system and standby system of a redundant system was detected. (This can be detected from the control system or standby system of the redundant system.)	Check the network cables of the MELSECNET/H multiplexed remote I/O network for disconnection.	QnPRH
	6040	In a redundant system, the memory card installation status (installed/not installed) differs between the control system and standby system.	Match the memory card installation statuses (set/not set) of the control system and standby system.	
	6041	In a redundant system, the memory card type differs between the control system and standby system.	Match the memory card types of the control system and standby system.	
	6050	The function inexecutable in the debug mode or operation mode (backup/separate mode) was executed. (This can be detected from the control system or standby system of the redundant system.)	Execute the function executable in the debug mode or operation mode (backup/separate mode).	QnPRH
	6060	In a redundant system, the operation mode (backup/separate) differs between the control system and standby system. (This can be detected from the standby system of the redundant system.)	Match the operation modes of the control system and standby system.	
	6061			
	6062	Both System A and B are in the same system status (control system). (This can be detected from the system B of the redundant system.)	Power the CPU module (System B) which resulted in a stop error, OFF and then ON.	QnPRH

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6100	TRUCKINERR.	–	–	On	On	Continue	At power ON/ At reset/ STOP→RUN	
	TRK. TRANS. ERR.	Tracking transmission data classification	–	On	On	Continue	Always	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6100	A CPU module tracking memory error was detected during initial. (This can be detected from the control system or standby system of the redundant system.)	Hardware fault of the CPU module. (Please contact your local nearest Mitsubishi or sales representative, explaining a detailed description of the problem. Change the CPU modules in order of the standby system CPU module and control system CPU module.)	Q4AR
		• An error (e.g. retry limit exceeded) occurred in tracking data transmission. (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH

*1 The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
6101	TRUCKIN ERR.	–	–	On	On	Continue	When an END instruction executed
	TRK. TRANS. ERR.	Tracking transmission data classification	–	On	On	Continue	Always
6102							
6103		–					

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		The CPU module detected an error during the handshake for tracking. (This can be detected from the control system or standby system of the redundant system.)	Check the condition of the other stations.	Q4AR
	6101	• A timeout error occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6102	A data sum value error occurred in tracking (data reception). (This can be detected from the control system or standby system of the redundant system.)		
	6103	• A data error (other than sum value error) occurred in tracking (data reception). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)		

*1 The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6105	TRK. TRANS. ERR.	Tracking transmission data classification	–	On	On	Continue	Always	
6106								
6107	TRK. TRANS. ERR.	–	–	On	On	Continue	Always	
6108								
6110	TRK. SIZE ERROR	Tracking capacity excess error factor	–	On	On	Continue	When an END instruction executed	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6105	• An error (e.g. retry limit exceeded) occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6106	• A timeout error occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)		
	6107	A data sum value error occurred in tracking (data reception). (This can be detected from the control system or standby system of the redundant system.)	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6108	• A data error (other than sum value error) occurred in tracking (data reception). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)		
	6110	The tracking capacity exceeded the allowed range. (This can be detected from the control system or standby system of the redundant system.)	Reexamine the tracking capacity.	QnPRH

*1   The module whose first 5 digits of serial No. is 09012 or later.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6111	TRK. SIZE ERROR	–	–	On	On	Continue	When an END instruction executed	
6112	TRK. SIZE ERROR	–	–	On	On	Continue	When an END instruction executed	
6120	TRK. CABLE ERR.	–	–	Off	Flicker	Stop	At power ON/ At reset	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6111	The control system does not have enough file register capacity for the file registers specified in the tracking settings. (This can be detected from the control system or standby system of the redundant system.)	Switch to the file registers of which capacity is greater than the file registers specified in the tracking settings.	QnPRH
	6112	File registers greater than those of the standby system were tracked and transmitted from the control system. (This can be detected from the standby system of the redundant system.)	Switch to the file registers of which capacity is greater than the file registers specified in the tracking settings.	
	6120	• A start was made without the tracking cable being connected. • A start was made with the tracking cable faulty. • As the tracking hardware on the CPU module side was faulty, communication with the other system could not be made via the tracking cable. (This can be detected from the control system or standby system of the redundant system.)	Make a start after connecting the tracking cable. If the same error still occurs, this indicates the tracking cable or CPU module side tracking transmission hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
6130	TRK. DISCONNECT	–	–	On	On	Continue	Always
6140	TRK.INIT. ERROR	–	–	Off	Flicker	Stop	At power ON/ At reset
6200	CONTROL EXE.	Reason(s) for system switching	–	On	Off	Continue	Always
	CONTROL EXE.	Reason(s) for system switching	–	On	Off	No error	Always

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6130	• The tracking cable was removed. • The tracking cable became faulty while the CPU module is running. • The CPU module side tracking hardware became faulty. (This can be detected from the control system or standby system of the redundant system.)	• If the tracking cable was removed, connect the tracking cable to the connectors of the CPU modules of the two systems. • When the error is not resolved after connecting the tracking cable to the connectors of the CPU modules of the two systems and resetting the error, the tracking cable or CPU module side tracking hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH
	6140	• The other system did not respond during initial communication at power ON/reset. • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the control system or standby system of the redundant system.)	• Power the corresponding CPU module OFF and then ON again, or reset it and then unreset. If the same error still occurs, this indicates the CPU module is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	
		The standby system in a redundant system is switched to the control system. (This can be detected from the standby system of the redundant system.)	Check the control system condition.	Q4AR
	6200	The standby system has been switched to the control system in a redundant system. (Detected by the CPU that was switched from the standby system to the control system) Since this error code does not indicate the error information of the CPU module but indicates its status, the error code and error information are not stored into SD0 to 26, but are stored into the error log every system switching. (Check the error information by reading the error log using GX Developer.)	–	QnPRH

*1　The module whose first 5 digits of serial No. is 09012 or later.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
6210	CONTROL WAIT	Reason(s) for system switching	–	On	Off	Continue	Always	
	STANDBY	Reason(s) for system switching	–	On	Off	No error	Always	
6220	CAN'T EXE. CHANGE	Reason(s) for system switching	–	On	On	Continue	At switching request	
	CAN'T SWITCH	Reason(s) for system switching	Reason(s) for system switching failure	On	On	Continue	At switching execution	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
		The control system in a redundant system is switched to the standby system. (This can be detected from the standby system of the redundant system.)	Check the control system condition.	Q4AR
	6210	The control system has been switched to the standby system in a redundant system. (Detected by the CPU that was switched from the control system to the standby system) Since this error code does not indicate the error information of the CPU module but indicates its status, the error code and error information are not stored into SD0 to 26, but are stored into the error log every system switching. (Check the error information by reading the error log using GX Developer.)	–	QnPRH
	6220	• Since the standby system is in an error or similar status in the redundant system, the control system cannot be switched to the standby system. • When an attempt was made to execute system switching, the control system could not be switched to the standby system due to a network error of the control system. (This can be detected from the control system of the redundant system.)	Check the standby system condition.	Q4AR
		System switching cannot be executed due to standby system error/ tracking cable error/ online module change in execution at separate mode. Causes for switching system at control system are as follows: • System switching by SP. CONTSW instruction • System switching request from network module	• Check the status of the standby system and resolve the error. • Complete the online module change.	QnPRH

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6221	CAN'T EXE. CHANGE	Reason(s) for system switching	–	On	On	Continue	At switching request	
6230	DUAL SYS. ERROR	–	–	On	On	Continue	Always	
6300	STANDBY SYS. DOWN	–	–	On	On	Continue	Always	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6221	Switching is disabled because of a bus switching module error. (This can be detected from the control system of the redundant system.)	This is a bus switching module hardware fault. (Contact your local Mitsubishi representative.)	Q4AR
	6230	The link module mounted on the standby system CPU module is the remote master station.	Check the system configuration status.	
	6300	Any of the following errors was detected in the backup mode. • The standby system has not started up in the redundant system. • The standby system has developed a stop error in the redundant system. • The CPU module in the debug mode was connected to the operating control system. (This can be detected from the control system of the redundant system.)	• Check whether the standby system is on or not, and if it is not on, power it on. • Check whether the standby system has been reset or not, and if it has been reset, unreset it. • Check whether the standby system has developed a stop error or not, and if it has developed the error, remove the error factor and restart it. • When the CPU module in the debug mode was connected to the control system operating in the backup mode, make connection so that the control system and standby system are combined correctly.	QnPRH

*1   The module whose first 5 digits of serial No. is 09012 or later.

| Error Code (SD0) | Error Message | Common Information (SD5 to 15) | Individual Information (SD16 to 26) | LED Status | | CPU Operation Status | Diagnostic Timing | |
				RUN	ERROR			
6310	CONTROL SYS. DOWN	–	–	Off	Flicker	Stop	Always	
6311	CONTROL SYS. DOWN	–	–	Off	Flicker	Stop	At power ON/ At reset	
6312								
6313	CONTROL SYS. DOWN	–	–	Off	Flicker	Stop	At power ON/ At reset	

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	6310	Any of the following errors was detected in the backup mode. • The control system has not started up in the redundant system. • The control system has developed a stop error in the redundant system. • The CPU module in the debug mode was connected to the operating standby system. • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the standby system of the redundant system.)	• The standby system exists but the control system does not exist. • Check whether the system other than the standby system is on or not, and if it is not on, power it on. • Check whether the system other than the standby system has been reset or not, and if it is has been reset, unreset it. • Check whether the system other than the standby system has developed a stop error or not, and if has developed the error, remove the error factor, set the control system and standby system to the same operating status, and restart. • When the CPU module in the debug mode was connected to the control system operating in the backup mode, make connection so that the control system and control system are combined correctly. • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6311	• As consistency check data has not transmitted from the control system in a redundant system, the other system cannot start as a standby system. • The error occurred at a startup since the redundant system startup procedure was not followed. (This can be detected from the standby system of the redundant system.)	• Replace the tracking cable. If the same error still occurs, this indicates the CPU module is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6312			
	6313	The control system detected the error of the system configuration and informed it to the standby system (host system) in the redundant system.	Restart the system after checking that the connection between base unit and the system configuration (type/number/parameter of module) are correct.	QnPRH[*1]

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
6400	PRG. MEM. CLEAR	–	–	Off	Flicker	Stop	At execution of the memory copy from control system to standby system	
6410	MEM.COPY EXE	–	–	On	On	Continue	At execution of the function of copying memory from control system to standby system	
6500	TRK. PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	
6501		File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	

Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
6400	The memory copy from control system to standby system was executed, and the program memory was cleared.	After the memory copy from control system to standby system is completed, switch power OFF and then ON, or make a reset.	QnPRH
6410	The memory copy from control system to standby system was executed. (This can be detected from the control system of the redundant system.)	–	
6500	The file register file specified in the tracking setting of the PLC parameter dialog box does not exist.	Read the individual information of the error using GX Developer, and check and correct the drive name and file name. Create the specified file.	QnPRH
6501	The file register range specified in the device detail setting of the tracking setting of the PLC parameter dialog box exceeded the specified file register file capacity.	Read the individual information of the error using GX Developer, and increase the file register capacity.	

*1   The module whose first 5 digits of serial No. is 09012 or later.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
7000	MULTI CPU DOWN	Module No. (CPU No.)	–	Off	Flicker	Stop	Always	
7002 7003	MULTI CPU DOWN	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	

*1   The function version is B or later.
*2   For the Basic model QCPU, the special register (SD207 to DS209) for LED indication priority can turn off the indication. (The LED indication is always OFF
       for the High Performance model QCPU, Process CPU, Redundant CPU, and Universal model QCPU.)

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	7000	• In the operating mode of a multiple CPU system, a CPU error occurred at the CPU where "All station stop by stop error of CPU " was selected. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted.	• Read the individual information of the error using the GX Developer, check the error of the PLC resulting in CPU module fault, and remove the error. • Remove the CPU module incompatible with the multiple CPU system from the main base unit.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
		In a multiple CPU system, CPU other than CPU No.1 cannot be started up due to stop error of the CPU No.1 at power-on, which occurs to CPU No.2 to No.4.	Read the individual information of the error using the GX Developer, check the error of the PLC resulting in CPU module fault, and remove the error.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
	7002	• There is no response from the target CPU module in a multiple CPU system during initial communication. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted.	• Reset the CPU module and RUN it again. If the same error is displayed again, this suggests the hardware fault of any of the CPU modules. (Contact your local Mitsubishi representative.) • Remove the CPU module incompatible with the multiple CPU system from the main base unit. Or, replace the CPU module incompatible with the multiple CPU system with the compatible one.	Q00/Q01[*1] Qn(H)[*1] QnPH
		• There is no response from the target CPU module in a multiple CPU system during initial communication.	• Reset the CPU module and RUN it again. If the same error is displayed again, this suggests the hardware fault of any of the CPU modules. (Contact your local Mitsubishi representative.)	QnU
	7003	There is no response from the target CPU module in a multiple CPU system at initial communication stage.	Reset the CPU module and RUN it again. If the same error is displayed again, this suggests the hardware fault of any of the CPU modules. (Contact your local Mitsubishi representative.)	Q00/Q01[*1] Qn(H)[*1] QnPH

*3   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
7004	MULTI CPU DOWN	Module No. (CPU No.)	–	Off	Flicker	Stop	Always	
7010	MULTI EXE. ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	

*1 The function version is B or later.
*2 For the Basic model QCPU, the special register (SD207 to DS209) for LED indication priority can turn off the indication. (The LED indication is always OFF for the High Performance model QCPU, Process CPU, Redundant CPU, and Universal model QCPU.)

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	7004	In a multiple CPU system, a data error occurred in communication between the CPU modules.	• Check the system configuration to see if modules are mounted in excess of the number of I/O points. • When there are no problems in the system configuration, this indicates the CPU module hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Q00/Q01[*1] QnU
	7010	• In a multiple CPU system, a faulty CPU module was mounted. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted. (The CPU module compatible with the multiple CPU system was used to detect an error.) • In a multiple CPU system, any of the CPU No. 2 to 4 was reset with power ON. (The CPU whose reset state was cancelled was used to detect an error.) • In a multiple CPU system, the PC CPU module used the QFB (bus interface driver) of version 1.06 or earlier.	• Read the individual information of the error using GX Developer, and replace the faulty CPU module. • Replace the CPU module with the one compatible with the multiple CPU system. • Do not reset any of the No. 2 to 4 CPU modules. • Use the PC CPU module that uses the QFB of version 1.07 or later. • Reset CPU No. 1 and restart the multiple CPU system.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU

*3   The Universal model QCPU except the Q02UCPU.

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Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing	
				RUN	ERROR			
7011	MULTI EXE. ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	
7013	MULTI EXE. ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	
7020	MULTI CPU ERROR	Module No. (CPU No.)	–	On	On	Continue	Always	
7030	CPU LAY. ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset	
7031								

*1   The function version is B or later.
*2   For the Basic model QCPU, the special register (SD207 to DS209) for LED indication priority can turn off the indication. (The LED indication is always OFF for the High Performance model QCPU, Process CPU, Redundant CPU, and Universal model QCPU.)

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	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	7011	Either of the following settings was made in a multiple CPU system. • Multiple CPU automatic refresh setting was made for the inapplicable CPU module. • "I/O sharing when using multiple CPUs" setting was made for the inapplicable CPU module.	• Correct the multiple CPU automatic refresh setting. • Correct the "I/O sharing when using multiple CPUs" setting.	Q00/Q01[*1] QnU
		The system configuration for using the Multiple CPU high speed transmission function is not met. • The QnUD(H)CPU is not used for the CPU No.1. • The Multiple CPU high speed main base unit (Q3□DB) is not used. • Points other than 0 is set to the send range for the CPU module incompatible with the multiple CPU high speed transmission function. • Points other than 0 is set to the send range for the CPU module incompatible with the multiple CPU.	Change the system configuration to meet the conditions for using the Multiple CPU high speed transmission function.	QnU[*3]
	7013	The Q172(H)CPU(N) or Q173(H)CPU(N) is mounted to the CPU slot or slots 0 to 2. (The module may break down.)	Remove the Q172(H)CPU(N) or Q173(H)CPU(N).	QnU
	7020	In the operating mode of a multiple CPU system, an error occurred in the CPU where "system stop" was not selected. (The CPU module where no error occurred was used to detect an error.)	Read the individual information of the error using the peripheral device, check the error of the CPU module resulting in CPU module fault, and remove the error.	Q00/Q01[*1] Qn(H)[*1] QnPH QnU
	7030	An assignment error occurred in the CPU-mountable slot (CPU slot, I/O slot 0, 1) in excess of the number of CPU modules specified in the multiple CPU setting of the PLC parameter dialog box.	• Set the same value to the number of CPU modules specified in the multiple CPU setting of the PLC parameter dialog box and the number of mounted CPU modules (including CPU (empty)). • Make the type specified in the I/O assignment setting of the PLC parameter dialog box consistent with the CPU module configuration.	Q00J/Q01/ Q01[*1] QnU
	7031	An assignment error occurred within the range of the number of CPUs specified in the multiple CPU setting of the PLC parameter dialog box.		

*3   The Universal model QCPU except the Q02UCPU.

Error Code (SD0)	Error Message	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		CPU Operation Status	Diagnostic Timing
				RUN	ERROR		
7032	CPU LAY. ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset
7035	CPU LAY. ERROR	Module No. (Slot No.)	–	Off	Flicker	Stop	At power ON/ At reset
7036	CPU RAY ERROR	Module No. (CPU No.)	–	Off	Flicker	Stop	At power ON/ At reset
8031	INCORRECT FILE	–	File diagnostic information	Off	Off	Stop	At power-On/ At reset/ STOP→RUN/ At PLC writing
9000	F****	Program error location	Annunciator number	On	On/ Off *2	Continue	When instruction executed
				USER LED On			
9010	<CHK>ERR ***-***	Program error location	Failure No.	On	Off	Continue	When instruction executed
				USER LED On			
9020	BOOT OK	–	–	Off	Flicker	Stop	At power ON/ At reset
10000	CONT. UNIT ERROR	–	–	Off	Flicker	Continue	Always

*1   The function version is B or later.
*2   For the Basic model QCPU, the special register (SD207 to DS209) for LED indication priority can turn off the indication. (The LED indication is always OFF for the High Performance model QCPU, Process CPU, Redundant CPU, and Universal model QCPU.)

	Error Code (SD0)	Error Contents and Cause	Corrective Action	Corresponding CPU
	7032	• The number of CPU modules mounted in a multiple CPU system is wrong.	Configure a system so that the number of mountable modules of each CPU module does not exceed the maximum number of mountable modules specified in the specification.	Q00/Q01[*1] QnU
	7035	The CPU module has been mounted on the inapplicable slot.	Mount the CPU module on the applicable slot.	Q00J/Q00/ Q01[*1] QnPRH QnU
	7036	The host CPU No. set by the multiple CPU setting and the host CPU No. determined by the mounting position of the CPU module are not the same.	• Mount the mounting slot of the CPU module correctly. • Correct the host CPU No. set by the multiple CPU setting to the CPU No. determined by the mounting position of the CPU module.	QnU[*3]
	8031	The error of stored file (enabled parameter file) is detected.	Write the file shown as SD17 to SD22 of individual information to the drive shown as SD16(L) of individual information, and turn ON from OFF the power supply of the CPU module or cancel the reset. If the same error is displayed again, the CPU module has hardware failure. Contact your local Mitsubishi representative, explaining a detailed description of the problem.	QnU
	9000	Annunciator (F) was set ON	Read the individual information of the error using the peripheral device, and check the program corresponding to the numerical value (annunciator number).	○
	9010	Error detected by the CHK instruction.	Read the individual information of the error using the peripheral device, and check the program corresponding to the numerical value (error number) there.	QnA Qn(H) QnPH QnPRH
	9020	Storage of data onto ROM was completed normally in automatic write to standard ROM. (BOOT LED also flickers.)	Use the DIP switches to set the valid parameter drive to the standard ROM. Then, switch power on again, and perform boot operation from the standard ROM.	Qn(H)[*1] QnPH QnPRH
	10000	In the multiple CPU system, an error occurred in the CPU module other than the Process CPU/High Performance model QCPU.	Check the details of the generated error by connecting to the corresponding CPU module using GX Developer.	Qn(H)[*1] QnPH

*3 The Universal model QCPU except the Q02UCPU.

5.4 Canceling of Errors

Q series CPU module can perform the cancel operation for errors only when the errors allow the CPU module to continue its operation.
To cancel the errors, follow the steps shown below.

   1) Eliminate the cause of the error.
   2) Store the error code to be canceled in the special register SD50.
   3) Energize the special relay SM50 (OFF → ON).
   4) The error to be canceled is canceled.

After the CPU module is reset by the canceling of the error, the special relays, special registers, and LEDs associated with the error are returned to the status under which the error occurred.
If the same error occurs again after the cancellation of the error, it will be registered again in the error history.

When multiple enunciators (F) detected are canceled, the first one with No. F only is canceled.

Refer to the following manual for details of error canceling.
 → QCPU User's Manual (Function Explanation, Program Fundamentals

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POINT

(1) When the error is canceled with the error code to be canceled stored in the SD50, the lower one digit of the code is neglected. (Example) If error codes 2100 and 2101 occur, and error code 2100 to cancel error code 2101.If error codes 2100 and 2111 occur, error code 2111 is not canceled even if error code 2100 is canceled.(2) Errors developed due to trouble in other than the CPU module are not canceled even if the special relay (SM50) and special register (SD50) are used to cancel the error.(Example)Since "SP. UNIT DOWN" is the error that occurred in the base unit (including the extension cable), intelligent function module, etc. the error cause cannot be removed even if the error is canceled by the special relay (SM50) and special register (SD50).Refer to the error code list and remove the error cause.

(2) Errors developed due to trouble in other than the CPU module are not canceled even if the special relay (SM50) and special register (SD50) are used to cancel the error.(Example)Since "SP. UNIT DOWN" is the error that occurred in the base unit (including the extension cable), intelligent function module, etc. the error cause cannot be removed even if the error is canceled by the special relay (SM50) and special register (SD50).Refer to the error code list and remove the error cause.

6. TRANSPORTATION PRECAUTIONS

When transporting lithium batteries, make sure to treat them based on the transport regulations.

6.1 Controlled Models
The batteries for the MELSEC-QnA series CPU module (including memory cards) are classified as follows:

Product name	Model	Product supply status	Classification for transportation
QnA series battery	A6BAT	Lithium battery	Non-dangerous goods
QnA series memory card	Q1MEM-128S, Q1MEM-128SE, Q1MEM-1MS, Q1MEM-1MSE, Q1MEM-1MSF, Q1MEM-256S, Q1MEM-256SE, Q1MEM-256SF, Q1MEM-2MS, Q1MEM-2MSF, Q1MEM-512S, Q1MEM-512SE, Q1MEM-512SF, Q1MEM-64S, Q1MEM-64SE	Packed with lithium coin battery (BR2325)	

6.2 Transport Guidelines
Comply with IATA Dangerous Goods Regulations, IMDG code and the local transport regulations when transporting products after unpacking or repacking, while Mitsubishi ships products with packages to comply with the transport regulations.
Also, contact the transporters.

MEMO

Warranty

Mitsubishi will not be held liable for damage caused by factors found not to be the cause of Mitsubishi; machine damage or lost profits caused by faults in the Mitsubishi products; damage, secondary damage, accident compensation caused by special factors unpredictable by Mitsubishi; damages to products other than Mitsubishi products; and to other duties.

⚠️For safe use

- This product has been manufactured as a general-purpose part for general industries, and has not been designed or manufactured to be incorporated in a device or system used in purposes related to human life.
- Before using the product for special purposes such as nuclear power, electric power, aerospace, medicine or passenger movement vehicles, consult with Mitsubishi.
- This product has been manufactured under strict quality control. However, when installing the product where major accidents or losses could occur if the product fails, install appropriate backup or failsafe functions in the system.

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